

10/5/1 (Item 1 from file: 6)  
DIALOG(R)File 6: NTIS  
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2152930 **NTIS Accession Number:** PB2000-102651/XAB  
**Analysis of Evolutionary Self-Organizing Map**

Nissinen, A. S. ; Hyoetyniemi, H.  
Helsinki Univ. of Technology, Espoo (Finland). Control Engineering Lab.

**Corporate Source Codes:** 057176020

**Report Number:** ISBN-951-22-4719-4

c1999 58p

**Language:** English

**Journal Announcement:** USGRDR0008

Also pub. as Helsinki Univ. of Technology, Espoo (Finland). Control Engineering Lab.  
rept. no. REPT-115.

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**NTIS Prices:** PC A05/MF A01

**Country of Publication:** Finland

Evolutionary self-organizing map (EVSOM) is an algorithm combining the competitive learning scheme of self-organizing maps and the search capabilities of evolutionary computing. Instead of using the parameter-oriented evaluation of 'objects', e.g. data vectors, it uses the survival or 'fitness' of an object with respect to some criterion. New results on applying EVSOM for modeling of dynamic systems are presented. Chapter 2 describes the distributed **memory** model, the inspiration of EVSOM. Also the structure, training algorithm and the role of parameters are shortly described. In Chapter 3, the EVSOM and SOM are compared to each other. Chapter 4 then considers the specific features of EVSOM algorithm. Chapter 5 describes how EVSOM is applied for modeling of dynamic **time-series** models. The new research topic is the use of cyclic **ring buffer** for storing data in order to enhance the parameter estimation process. Chapter 6 gives an example, where EVSOM is used for modeling a system with changing underlying structure. Chapter 7 then presents an example with continuous non-linearity. The summary is given in Chapter 8.

**Descriptors:** \*Dynamic systems; \*Algorithms; Self organizing systems; Data structures; Nodes; Distributed **memory**; **Time series** analysis; Neural networks

**Identifiers:** \*Foreign technology; \*Evolutionary self-organizing map; SOM(Self-Organizing Map); NTISTFHUT

**Section Headings:** 62GE (Computers, Control, and Information Theory--General)

10/5/2 (Item 1 from file: 60)  
DIALOG(R)File 60: ANTE: Abstracts in New Tech & Engineer  
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0002126714 IP Accession No: 20082062205

**Digital camera with time bracketing feature**

Kerns, Charles; Zarakov, Eric; Gilley, Thomas S  
, USA

**Publisher Url:** <http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO2&Sect2=HITOFF&u=/netaht ml/PTO/search-adv.htm&r=1&p=1&f=G&l=50&d=PTXT&S1=53 67332.PN.&OS=pn/5367332&RS=PN/5367332>

**Document Type:** Patent

**Record Type:** Abstract

**Language:** English

**File Segment:** ANTE: Abstracts in New Technologies and Engineering

**Abstract:**

A digital still image camera having a **memory** means functioning as a **ring buffer**. Image and sound acquisition and **storage** begins upon a first command signal from the camera's operator and continues at a defined rate until the operator generates a second command signal. During the **time period** between the first and second command signal, images are taken and stored at the defined rate, the newest image overwriting the oldest image within the **ring buffer** after the **ring buffer** is initially filled. After generating the second command signal, the operator can review the contents of the **ring buffer** on a display and speaker incorporated into the camera, selecting those images and sounds the operator wishes to store permanently, and then directing that the selected images and sounds be placed in the camera's long term **storage memory**.

**Descriptors:** Images; Buffers; Cameras; Operators; Sound; Marketing; Digital cameras; Macintosh personal computers; Acquisitions

10/5/3 (Item 2 from file: 60)

DIALOG(R)File 60: ANTE: Abstracts in New Tech & Engineer

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0001317529 IP Accession No: 20081024455

**Digital data reproducing apparatus and method employing buffer memories for error compensation and accomodating differences in reproduction and processing rates**

Yonemitsu, Jun; Koyanagi, Hideki; Akiyama, Yoshiyuki  
, USA

**Publisher Url:** <http://patft.uspto.gov/netacgi/nph-Parser?Sect1=PTO2&Sect2=HITOFF&u=/netaht ml/PTO/search-adv.htm&r=1&p=1&f=G&l=50&d=PTXT&S1=54 71450.PN.&OS=pn/5471450&RS=PN/5471450>

**Document Type:** Patent

**Record Type:** Abstract

**Language:** English

**File Segment:** ANTE: Abstracts in New Technologies and Engineering

**Abstract:**

A digital data reproducing apparatus and method which allows reproduction for a long **period of time** without deterioration in picture quality. Data are reproduced from an optical disk by a pickup and are transferred to and stored in a **ring buffer memory**. Data read from the **ring buffer memory** are transferred to and stored into a video code buffer of an encoder section. The data stored in the video code buffer are encoded in the encoder section and displayed on a display. A track jump judging circuit produces a track jumping instruction to cause the pickup to track jump back to a preceding track when the amount of data stored in the **ring buffer memory** reaches a predetermined value.

**Descriptors:** Buffers; Digital data; Encoders; Reproduction; Coders; Optical discs; Deterioration; Circuits; Error compensation

15/5/1 (Item 1 from file: 347)  
DIALOG(R)File 347: JAPIO  
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09070489 \*\*Image available\*\*

## **TIME SERIES SIGNAL INPUT METHOD**

**Pub. No.:** 2007-110749 [JP 2007110749 A ]

**Published:** April 26, 2007 (20070426)

**Inventor:** FUJIKAWA YOSHIBUMI

ISHIGURO MASAO

KAWAGUCHI ATSUO

**Applicant:** HITACHI LTD

**Application No.:** 2006-339394 [JP 2006339394]

Division of 2001-338864 [JP 2001338864]

**Filed:** December 18, 2006 (20061218)

**International Patent Class (v8 + Attributes)**

**IPC + Level Value Position Status Version Action Source Office:**

H04N-0007/26

A I F B 20060101 20070330 H JP

## **ABSTRACT**

**PROBLEM TO BE SOLVED:** To solve a problem that synchronization between a video and a sound cannot be established because a data input time cannot be predicted by software after input processing software due to the variation of a software processing time when a video-sound processing system is equipped with many pieces of software.

**SOLUTION:** **Time series** data input middleware includes an input data ring buffer and a transfer start time variable, and calculates an input time of each data in the input data **ring buffer** using a system time **register** and the write address information of a DMA transfer control apparatus. Input data and input time information are returned in response to a data request 5 from a **time series** data processing module.

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15/5/2 (Item 2 from file: 347)

DIALOG(R)File 347: JAPIO

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07649708 \*\*Image available\*\*

## **TIME SERIES SIGNAL INPUT METHOD**

**Pub. No.:** 2003-143563 [JP 2003143563 A ]

**Published:** May 16, 2003 (20030516)

**Inventor:** FUJIKAWA YOSHIBUMI

ISHIGURO MASAO

KAWAGUCHI ATSUO

**Applicant:** HITACHI LTD

**Application No.:** 2001-338864 [JP 2001338864]

**Filed:** November 05, 2001 (20011105)

**International Class:** H04N-007/08; H04N-007/081; H04N-007/14

## ABSTRACT

**PROBLEM TO BE SOLVED:** To give an accurate input time to each encoding module to synchronize pictures and sounds with each other as a result.

**SOLUTION:** **Time series** data input middleware is provided with an input data **ring buffer** and transfer start time variables, and a system time **register** and write address information of a DMA transfer controller are used to calculate the input time of each data in the input data ring buffer. Input data and input time information are returned in response to a data request 5 from a **time series** data processing module.

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15/5,K/3 (Item 1 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0018760410 *Drawing available*

WPI Acc no: 2009-F30795/200917

**Data storage equipment of display device of alternating current servo system, stores data at different levels in ring buffer, according to time period of measurement of data**

Patent Assignee: MATSUSHITA DENKI SANGYO KK (MATU)

Inventor: IKEUCHI Y; IMADA Y; SUZUKI K

Patent Family ( 1 patents, 1 countries )

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
JP 2009042981	A	20090226	JP 2007206555	A	20070808	200917	B

Priority Applications (no., kind, date): JP 2007206555 A 20070808

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
JP 2009042981	A	JA	10	4	

#### Alerting Abstract JP A

NOVELTY - The data storage equipment stores data measured during different **periods of time**, at different levels in a ring buffer according to the measured **period of time**.

DESCRIPTION - An INDEPENDENT CLAIM is included for display device.

USE - Data storage equipment of display device of alternating current servo system.

ADVANTAGE - The memory capacity of the storage equipment can be used efficiently.

DESCRIPTION OF DRAWINGS - The drawing shows an explanatory view illustrating the storage of data in data storage equipment. (Drawing includes non-English language text)

**Title Terms /Index Terms/Additional Words:** DATA; STORAGE; EQUIPMENT; DISPLAY; DEVICE; ALTERNATE; CURRENT; SERVO; SYSTEM; LEVEL; RING; BUFFER; ACCORD; TIME; PERIOD; MEASURE

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G05B-0023/02	A	I	F	B	20060101
G05B-0023/02	C	I		B	20060101

File Segment: EPI;

DWPI Class: T06

Manual Codes (EPI/S-X): T06-A08

...of alternating current servo system, stores data at different levels in ring buffer, according to time period of measurement of data **Alerting Abstract** ...NOVELTY - The data storage equipment stores data measured during different **periods of time**, at different levels in a ring buffer according to the measured **period of time**. Original Publication Data by AuthorityArgentina**Publication No.** ...**Original Abstracts:**than or equal to 1), and about 2k\*T0 of the period longest A 2n piece,It is data storage equipment distributed and preservelsaved about another period at the **ring buffer** with the magnitudelsize of a 2(n-1) piece (n is an integer greater...

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15/5,K/4 (Item 2 from file: 350)

DIALOG(R)File 350: Derwent WPIX  
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0017656152 *Drawing available*  
WPI Acc no: 2008-E76594/200833  
XRPX Acc No: N2008-374660

**Positioning device for e.g. portable telephone, has positioning section performing acquisition and/or tracking process of global positioning system satellite signal based on read signal, and performing specific positioning process**

Patent Assignee: SEIKO EPSON CORP (SHIH)

Inventor: KIMURA A

Patent Family ( 6 patents, 41 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 1916536	A2	20080430	EP 200717466	A	20070906	200833	B
JP 2008107220	A	20080508	JP 2006290819	A	20061026	200833	E
CN 101169478	A	20080430	CN 200710165524	A	20071026	200848	E
KR 2008037517	A	20080430	KR 200793628	A	20070914	200868	E
US 20080252520	A1	20081016	US 2007839624	A	20070816	200869	E
JP 4229169	B2	20090225	JP 2006290819	A	20061026	200918	E

Priority Applications (no., kind, date): JP 2006290819 A 20061026

Patent Details						
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes	
EP 1916536	A2	EN	23	12		
Regional Designated States,Original	AL AT BA BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK MT NL PL PT RO RS SE SI SK TR					
JP 2008107220	A	JA	19			
JP 4229169	B2	JA	19		Previously issued patent	JP 2008107220

#### Alerting Abstract EP A2

NOVELTY - The device (100) has a radio frequency reception circuit section (60) receiving a global positioning system (GPS) satellite signal, where the signal is sequentially stored in a **storage** area of a memory (70) i.e. **ring buffer**, while shifting a **storage** position. A positioning section reads another signal from the **storage** area, while changing a signal read position. The positioning section performs an acquisition and/or tracking process of the satellite signal based on the read signal, and performs a specific

positioning process.

DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

1. a program for positioning an electronic instrument
2. a computer-readable storage medium storing the program for positioning an electronic instrument.

USE - Positioning device for use in an electronic instrument (claimed) i.e. portable telephone, for positioning the electronic instrument. Can also be used for a personal digital assistant, portable navigation system and car navigation system.

ADVANTAGE - The positioning device can accurately estimate polarity inversion position of the navigation message, while reducing processing time.

DESCRIPTION OF DRAWINGS - The drawing shows a block diagram of the configuration of a portable telephone.

60 Radio frequency reception circuit section

70 Memory

100 Positioning device

120 Operation section

130 Display section

**Title Terms /Index Terms/Additional Words:** POSITION; DEVICE; PORTABLE; TELEPHONE; SECTION; PERFORMANCE; ACQUIRE; TRACK; PROCESS; GLOBE; SYSTEM; SATELLITE; SIGNAL; BASED; READ; SPECIFIC

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G01S-0001/00	A	I	F	B	20060101
G01S-0005/14	A	I	F	B	20060101
G01S-0005/14	A	I	F		20060101
G01S-0001/00	C	I	F	B	20060101
G01S-0005/14	C	I	F	B	20060101
G01S-0005/14	C	I			20060101
G01S-0005/14	C	I		B	20060101

ECLA: G01S-001/00S2C13, G01S-001/00S2C6

US Classification, Current Main: 342-357150

US Classification, Issued: 342357.15

File Segment: EPI;

DWPI Class: T01; W01; W06

Manual Codes (EPI/S-X): T01-J07D1; T01-M06A1A; T01-S03; W01-C01D3C; W01-



**Alerting Abstract** ...a global positioning system (GPS) satellite signal, where the signal is sequentially stored in a **storage** area of a memory (70) i.e. **ring buffer**, while shifting a **storage** position. A positioning **section** reads another signal from the **storage** area, while changing a signal read position. The positioning section performs an acquisition and/or...

Original Publication Data by Authority Argentina Publication No. ...**Original**

**Abstracts:**invention claims a positioning device, a slicing set with stated time is obtained from a **storage** area of a **memory** which is a **ring buffer** while changing the read position, and the signal strength total value of each slicing set... ... A slice set for a specific **period of time** is acquired from a **storage** area of a **memory** which is a **ring buffer** while changing the read position, and the signal strength total value of each slice set... ... the slice group for predetermined time is acquired from the storing arealregion of the **memory** which is a **ring buffer**, and the signal-strength total value of each of the said slice group is calculated... ... The reading position is shifted from the **storage** area of the **memory** of being the **ring buffer** and the slice tub of the proper time amount is obtained. The corresponding slice tub... ... A slice set for a specific **period of time** is acquired from a **storage** area of a **memory** which is a **ring buffer** while changing the read position, and the signal strength total value of each slice set... **Claims:** A positioning device, comprising a **storage** that is the **ring buffer**, it has the **storage** area comprising the signal of the reversing **time interval** that at least equal or more than three cycle navigation, one side change the storage... ... A positioning device comprising:a **memory** which is a **ring buffer** which includes a **storage** area capable of storing three or more cycles of a signal corresponding to at least an inversion **time interval** of a navigation message and in which a signal received by an RF reception circuit section which receives a GPS satellite signal is sequentially stored while shifting a **storage** position; and a positioning **section** which reads the signal from the **storage** area while changing a signal read position, performs an acquisition and/or tracking process of... ... The storing domain which can store 3 cycles or more of signals for an inversion **time** spaceinterval of a navigation message at leastIt is the storing position about the signal received by RF receiving circuit part which has a region and receives a GPS Satellite signal.**Memory** which is the **ring buffer** stored sequentially shifting,A signal read-out position is varied and read from the said... ... storing arealregion which can store 3 cycles or more of signals for the inversion **time** spaceinterval of a navigation message at least.**Memory** which is the **ring buffer** which stores sequentially the signal received by the RF receiving circuit unit which receives a... ... least, the positioning apparatus having the storage area can store the signal of the inversion **time interval** of the navigation message over 3 cycle, and for including the **memory** of being the **ring buffer** stored as successively, and the acquire of the GPS satellite signal about the read-out...CLAIM 5] The **memory** of being the **ring buffer**, the group signal strength output unit signing the proper time amount signal of N (Nb... ...the location are included with the RF receiving circuit receiving the GPS satellite signal. The **memory** of being the **ring buffer** successively stores the received signal. As to the decision unit, the proper time amount signal... ...the storage media having the storage area which can store the signal of the inversion **time interval** of the navigation message over 3 cycle, and varies the signal reading location from the , storage area and reads the processor of the apparatus which includes the **memory** of being the **ring buffer** stored as

successively, and the processor while shifting the housing position the signal received with... as the signal of the proper time amount and is the program obtained and the **memory** of being the **ring buffer** which successively stores the signal received with the RF receiving circuit receiving the GPS satellite... ... What is claimed is: 1. A positioning device comprising: a **memory** which is a **ring buffer** which includes a **storage area** capable of storing three or more cycles of a signal corresponding to at least an inversion **time interval** of a navigation message and in which a signal received by an RF ... reception circuit section which receives a GPS satellite signal is sequentially stored while shifting a **storage** position; and a positioning **section** which reads the signal from the **storage** area while changing a signal read position, performs an acquisition and/or tracking process of...

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15/5,K/5 (Item 3 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0017561186 *Drawing available*

WPI Acc no: 2008-D81628/200827

XRPX Acc No: N2008-298135

**Semiconductor memory device i.e. ferro-electric RAM, for use in data e.g. binary data, storage device, has control section controlling polarization of memory cell in polarization states before writing data to memory cells**

Patent Assignee: SEIKO EPSON CORP (SHIH)

Inventor: FUKADA S

Patent Family ( 2 patents, 2 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20080080225	A1	20080403	US 2007865095	A	20071001	200827	B
JP 2008090933	A	20080417	JP 2006270757	A	20061002	200829	E

Priority Applications (no., kind, date): JP 2006270757 A 20061002

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20080080225	A1	EN	19	7	
JP 2008090933	A	JA	23		

**Alerting Abstract** US A1

NOVELTY - The device has a control section controlling writing and reading of data with respect to a **memory section**. The **memory section** includes **memory region** (12) formed from nonvolatile **memory cells** i.e. ferroelectric memory cell, where each cells

stores binary data corresponding to two polarization states. The control **section** controls polarization of the **memory** cell in the polarization states before writing data to each of the memory cells based on new data externally inputted for all of the memory cells included in the memory region.

DESCRIPTION - INDEPENDENT CLAIMS are also included for the following:

1. a data storage device comprising a semiconductor memory device
2. a method for controlling a semiconductor memory device.

USE - Semiconductor memory device i.e. ferro-electric RAM (FeRAM), for use in a data storage device (claimed) that is used for collecting and storing a data such as binary data and **time-series** data, and in an aircraft black box and seismometer.

ADVANTAGE - The device eliminates need to maintain the polarization direction for a long time when overwriting events frequently occur, thus suppressing occurrence of imprint without increasing a storage capacity and improving reliability of the memory device.

DESCRIPTION OF DRAWINGS - The drawing shows a schematic view used for describing a concrete example of a composition for controlling write and readout operations when a **memory** region is composed as a **ring buffer**.

M1, M3, M4, M5, M6 Memory area

P1 Data write pointer

P2 Data readout pointer

P3 Polarization state write pointer

P4 Empty readout pointer

T1-T3 Predetermined time

12 Memory region

**Title Terms /Index Terms/Additional Words:** SEMICONDUCTOR; MEMORY; DEVICE; FERRO; ELECTRIC; RAM; DATA; BINARY; STORAGE; CONTROL; SECTION; POLARISE; CELL; STATE; WRITING

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G11C-0011/22	A	I	F	B	20060101
G11C-0007/00	A	I	L	B	20060101
G11C-0011/22	C	I	F	B	20060101
G11C-0007/00	C	I	L	B	20060101

ECLA: G11C-011/22

ICO: S11C-007:24

US Classification, Current Main: 365-145000; Secondary: 365-189140

US Classification, Issued: 365145, 365189.14  
File Segment: EPI;  
DWPI Class: U14  
Manual Codes (EPI/S-X): U14-A03F; U14-A07

...memory device i.e. ferro-electric RAM, for use in data e.g. binary data, storage device, has control section controlling polarization of memory cell in polarization states before writing data to memory cells Alerting Abstract ...device has a control section controlling writing and reading of data with respect to a **memory section**. The **memory section** includes **memory** region (12) formed from nonvolatile **memory** cells i.e. ferroelectric memory cell, where each cells stores binary data corresponding to two polarization states. The control **section** controls polarization of the **memory** cell in the polarization states before writing data to each of the memory cells based ... .. claimed) that is used for collecting and storing a data such as binary data and **time-series** data, and in an aircraft black box and seismometer.... .. describing a concrete example of a composition for controlling write and readout operations when a **memory** region is composed as a **ring buffer**.Original Publication Data by AuthorityArgentinaPublication No. Original Abstracts:A semiconductor **memory** device includes: a **memory section**; and a control **section** that controls writing and reading of data with respect to the **memory section**, wherein the **memory section** includes a first **memory** region formed from nonvolatile memory cells, each of the memory cells storing binary data corresponding to a first polarization state and a second polarization state; and the control **section** controls, for all of the **memory** cells included in the first memory region, such that, before writing data to each of... ..Claims:What is claimed is:1. A semiconductor **memory** device comprising:a **memory section**; anda control **section** that controls writing and reading of data with respect to the **memory section**,wherein the **memory section** includes a first **memory** region formed from nonvolatile memory cells, each of the memory cells storing binary data corresponding to a first polarization state and a second polarization state; andthe control **section** controls, for all of the **memory** cells included in the first memory region, such that, before writing data to each of...

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15/5,K/6 (Item 4 from file: 350)  
DIALOG(R)File 350: Derwent WPIX  
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0016822767 *Drawing available*  
WPI Acc no: 2007-537828/200753  
Related WPI Acc No: 2003-413446  
XRPX Acc No: N2007-413960

**Time sequential signal input method for use in video processing system, involves returning input data and time information according to data request from time sequential data processing module**  
Patent Assignee: HITACHI LTD (HITA)  
Inventor: FUJIKAWA Y; ISHIGURO M; KAWAGUCHI A

Patent Family ( 1 patents, 1 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
JP 2007110749	A	20070426	JP 2001338864	A	20011105	200753	B
			JP 2006339394	A	20061218		

Priority Applications (no., kind, date): JP 2001338864 A 20011105; JP 2006339394 A 20061218

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
JP 2007110749	A	JA	11	13	Division of application JP 2001338864

#### Alerting Abstract JP A

**NOVELTY** - The method involves calculating time of data input in a **ring buffer** (10) using write-in address information of system time **register** (9) and direct memory access (DMA) transmission control apparatus (8). The input data and time information are returned according to data request from a time sequential data processing module (6).

**DESCRIPTION** - **INDEPENDENT CLAIMS** are included for the following:

1. time-sequential signal input device;
2. recording medium;
3. video signal input method;
4. video signal input device;
5. audio signal input method; and
6. audio signal input device.

**USE** - For encoding processing apparatus used in media processing systems, such as digital video recording apparatus and video conference system.

**ADVANTAGE** - The processing time of each software is varied, estimation of data entry time is disabled after input processing and audio-video synchronization is enabled. An effective video compression encoding module is obtained, and decimation of process data by argument intervals is performed effectively when removing frame rate.

**DESCRIPTION OF DRAWINGS** - The figure shows a block diagram of the time sequential data entry middleware. (Drawing includes non-English language text)

- 1 Time sequential data entry middleware
- 6 Time sequential data processing module
- 8 Direct memory access transmission control apparatus
- 9 System time register
- 10 Ring buffer

**Title Terms /Index Terms/Additional Words:** TIME; SEQUENCE; SIGNAL; INPUT; METHOD; VIDEO ; PROCESS; SYSTEM; RETURN; DATA; INFORMATION; ACCORD; REQUEST; MODULE

## Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
H04N-0007/26	A	I	F	B	20060101
H04N-0007/26	C	I		B	20060101

## Japan National Classification FI Terms

FI Term	Facet	Rank	Type
H04N-007/13 Z			

## Japan National Classification F Terms

Theme	ViewPoint + Figure	Additional Code
5C059		
5C059	KK33	
5C059	LB07	
5C059	PP04	
5C059	RC04	
5C059	RC32	
5C059	RE03	
5C059	SS20	
5C059	SS26	
5C059	UA02	

File Segment: EPI;

DWPI Class: T01; W03; W04

Manual Codes (EPI/S-X): T01-H01A; T01-H05B2; T01-J10D; T01-S03; W03-A11D; W04-F01F; W04-F01M; W04-P01A; W04-P01C5

## Original Titles: TIME SERIES SIGNAL INPUT METHOD Alerting Abstract

...NOVELTY - The method involves calculating time of data input in a **ring buffer** (10) using write-in address information of system time **register** (9) and direct memory access (DMA) transmission control apparatus (8). The input data and time...

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15/5.K/7 (Item 5 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0014553875 *Drawing available*

WPI Acc no: 2004-735833/200472

XRFX Acc No: N2004-582218

**Program data e.g. audio/video data, recording and reproduction apparatus, has time shift region including read and write position related sectors each with altered attributes during recording operation**

Patent Assignee: FUNAI DENKI KK (FUNA-N); SUGIHARA A (SUGI-I); FUNAI ELECTRIC CO LTD (FUNA-N)

Inventor: SUGIHARA A

Patent Family ( 4 patents, 2 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20040190858	A1	20040930	US 2004807609	A	20040324	200472	B
JP 2004295963	A	20041021	JP 200384369	A	20030326	200472	E
JP 3922199	B2	20070530	JP 200384369	A	20030326	200737	E
US 7349622	B2	20080325	US 2004807609	A	20040324	200823	E

Priority Applications (no., kind, date): JP 200384369 A 20030326; US 2004807609 A 20040324

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20040190858	A1	EN	8	5	
JP 2004295963	A	JA	11		
JP 3922199	B2	JA	11		Previously issued patent JP 2004295963

#### Alerting Abstract US A1

**NOVELTY** - The apparatus has a hard disk (12) with a time shift region and a recording region that records information for long **period of time**. The **time** shift region has a read position (RP) associated sector through a write position (WP) associated sector having respective attributes altered into the recording region when the portions are performing time shift playback and receiving an instruction to record program data.

**DESCRIPTION** - An **INDEPENDENT CLAIM** is also included for a method of recording and reproducing data including video and audio data.

**USE** - Used for recording and reproducing program data e.g. audio/video data.

**ADVANTAGE** - The time shift region includes read and write position related sectors each with altered attributes during recording operation, thus effecting time shift playback without a cumbersome operation in changing recording and reproduction points.

**DESCRIPTION OF DRAWINGS** - The drawing shows a configuration of an information recording and reproduction apparatus.

RP Read position  
 WP Write position  
 10 System control circuit  
 11 Tuner  
 12 Hard disk derive

**Title Terms /Index Terms/Additional Words:** PROGRAM; DATA; AUDIO; VIDEO; RECORD; REPRODUCE; APPARATUS; TIME; SHIFT; REGION; READ; WRITING; POSITION; RELATED ; SECTOR; ALTER; ATTRIBUTE; OPERATE

**Class Codes**

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G11B-0020/10	A	I	F	B	20060101
G11B-0020/10	A	I		R	20060101
G11B-0027/10	A	I		R	20060101
H04N-0005/00	A	I	L	B	20060101
H04N-0005/76	A	I		R	20060101
H04N-0005/781	A	N		R	20060101
H04N-0005/92	A	I	F	R	20060101
H04N-0007/00	A	I	F	B	20060101
H04N-0009/804	A	N		R	20060101
G11B-0020/10	C	I	F	B	20060101
G11B-0020/10	C	I		R	20060101
G11B-0027/10	C	I		R	20060101
H04N-0005/00	C	I	L	B	20060101
H04N-0005/76	C	I		R	20060101
H04N-0005/781	C	N		R	20060101
H04N-0005/92	C	I	F	R	20060101
H04N-0007/00	C	I	F	B	20060101
H04N-0009/804	C	N		R	20060101

**ECLA:** G11B-020/10A, G11B-027/10A1, H04N-005/76

**ICO:** T04N-005:781, T04N-009:804B

**US Classification, Current Main:** 386-046000; Secondary: 386-E05001

**US Classification, Issued:** 38646, 38646, 386125



Japan National Classification FI Terms			
FI Term	Facet	Rank	Type
G11B-020/10 301 Z			
G11B-020/10 A			
H04N-005/92 H			

Japan National Classification F Terms		
Theme	ViewPoint + Figure	Additional Code
5C053		
5D044		
5D044	AB05	
5D044	AB07	
5D044	BC01	
5D044	BC04	
5D044	CC04	
5D044	DE03	
5D044	DE96	
5D044	EF03	
5C053	FA20	
5C053	FA23	
5D044	FG10	
5D044	FG23	
5C053	GA11	
5C053	GB37	
5D044	GK11	
5C053	HA26	
5C053	LA06	
5C053	LA07	
5C053	LA15	

File Segment: EPI;

DWPI Class: W04

Manual Codes (EPI/S-X): W04-B14C3; W04-E20J; W04-F01H3

**Alerting Abstract ...12)** with a time shift region and a recording region that records information for long **period of time**. The **time** shift region has a read position (RP) associated sector through a write position (WP) associated... Original Publication Data by

Authority Argentina **Publication No. ...Claims:** said recording portion; a region management portion said recording portion by dividing said recording **portion** into a temporary **storage** region provided in a form of a **ring buffer** to temporarily record said program data, and a long term **storage** region recording said program data for a long **period of time**; and a control portion causing said recording portion and said reproduction portion to at least... ... and said input portion has received an instruction to record program data for a long **period of time** said region management **portion** effecting management to alter said temporary **storage** region at a region containing a **portion** having said program data being reproduced through a portion recording said program data into said... ... said recording portion; a region management portion managing said recording portion by dividing said recording **portion** into a temporary **storage** region provided in a form of a **ring buffer** to temporarily record said program data, and a long term **storage** region recording said program data for a long **period of time**; and a control portion causing said recording portion and said reproduction portion to at least... ... and said input portion has received an instruction to record program data for a long **period of time** said region management **portion** effecting management to alter said temporary **storage** region at a region containing a **portion** having said program data being reproduced through a portion recording said program data into said long term **storage** region; wherein when said region management **portion** alters said long term **storage** region into said temporary **storage** region more than once said region management **portion** alters into said temporary **storage** region a region of said long term storage region adjacent to a region having been...

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15/5.K/8 (Item 6 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0014232427 *Drawing available*

WPI Acc no: 2004-418367/200439

XRPX Acc No: N2004-332004

**Paper jam detector for image forming device e.g. laser printer associates signal indicating status of image forming unit with time series data in storage unit**

Patent Assignee: BROTHER KOGYO KK (BRER)

Inventor: ENDO Y

Patent Family ( 4 patents, 33 countries )

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20040086284	A1	20040506	US 2003694750	A	20031029	200439	B
EP 1418533	A2	20040512	EP 200324870	A	20031031	200439	E
JP 2004148690	A	20040527	JP 2002316953	A	20021031	200441	E
US 7140707	B2	20061128	US 2003694750	A	20031029	200680	E

Priority Applications (no., kind, date): JP 2002316953 A 20021031; US 2003694750 A 20031029

Patent Details					
Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 20040086284	A1	EN	19	5	
EP 1418533	A2	EN			
Regional Designated States, Original	AL AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LT LU LV MC MK NL PT RO SE SI SK TR				
JP 2004148690	A	JA	21		

#### Alerting Abstract US A1

NOVELTY - A storage control unit associates signals from paper sensors indicating the status of image forming unit with **time series** data in storage unit.

USE - Image forming device e.g. laser printer and inkjet printer.

ADVANTAGE - Enables the operator to easily analyze generated errors by linking several sensors of signal generating unit and operating elements of image forming unit and decide exactly what has caused the paper jam.

DESCRIPTION OF DRAWINGS - The figure shows the block diagram of the laser printer.

51 engine

57 operating element

58 sensor

**Title Terms** /Index Terms/Additional Words: PAPER; JAM; DETECT; IMAGE; FORMING; DEVICE; LASER; PRINT; ASSOCIATE; SIGNAL; INDICATE; STATUS; UNIT; TIME; SERIES; DATA ; STORAGE

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
B41J-0011/00	A	I		R	20060101
B41J-0029/38	A	I	F	B	20060101
B41J-0029/38	A	I	F	R	20060101
B41J-0029/393	A	I	L	B	20060101
B41J-0029/393	A	I		R	20060101
G03G-0021/00	A	I	L	R	20060101
G06F-0003/12	A	I	L	R	20060101
G06K-0015/00	A	I		R	20060101
B41J-0011/00	C	I		R	20060101
B41J-0029/38	C	I	F	B	20060101
B41J-0029/38	C	I	F	R	20060101
B41J-0029/393	C	I	L	B	20060101
B41J-0029/393	C	I		R	20060101
G03G-0021/00	C	I	L	R	20060101
G06F-0003/12	C	I	L	R	20060101
G06K-0015/00	C	I		R	20060101

**ECLA:** B41J-011/00J, B41J-029/393, G06K-015/00

**ICO:** S06K-215:00B2H, S06K-215:00D2

**US Classification, Current Main:** 399-010000

**US Classification, Issued:** 39910, 34714, 34719

Japan National Classification FI Terms			
FI Term	Facet	Rank	Type
B41J-029/38 Z			
G03G-021/00 370			
G03G-021/00 376			
G06F-003/12 K			

Japan National Classification F Terms

Theme	ViewPoint + Figure	Additional Code
2C061		
2H027		
5B021		
5B021	AA02	
5B021	AA21	
2C061	AP01	
2C061	AQ05	
2C061	AQ06	
5B021	BB01	
5B021	CC05	
2H027	DA12	
2H027	DA26	
2H027	DA38	
2H027	DA50	
2H027	DB07	
2H027	DC14	
2H027	DC15	
2H027	DC16	
2H027	DC17	
2H027	DC18	
2H027	DD02	
2H027	EE08	
2H027	EE10	
2H027	FA30	
2H027	HA06	
2H027	HA10	
2H027	HA12	
2H027	HB17	
2C061	HK11	
2C061	HV60	
5B021	NN17	
5B021	NN19	
2H027	ZA07	

File Segment: EngPI; EPI;

DWPI Class: S06; T04; P75

Manual Codes (EPI/S-X): S06-A12B; S06-A14B; T04-G06A; T04-G10A

...forming device e.g. laser printer associates signal indicating status of image forming unit with time series data in storage unit Alerting Abstract ...control unit associates signals from paper sensors indicating the status of image forming unit with time series data in storage unit. Original Publication Data by AuthorityArgentinaPublication No. Original Abstracts:By executing a storage control program, a CPU core (59) stores in a time series detection signals from sensors (58), operation signals outputted to operating members (57), and error detection results in a ring buffer (73) of a RAM (55) whenever a change occurs in any of these data. The CPU core (59) outputs... ... By executing a storage control program, a CPU core stores in a time series detection signals from sensors, operation signals outputted to operating members, and error detection results in a ring buffer of a RAM whenever a change occurs in any of these data. The CPU core outputs the data... ... By executing a storage control program, a CPU core stores in a time series detection signals from sensors, operation signals outputted to operating members, and error detection results in a ring buffer of a RAM whenever a change occurs in any of these data. The CPU core outputs the data... ...Claims:generating portion generating a status-indicating signal indicative of a status of the image forming portion;a storage portion storing data of the status-indicating signal; anda storage control portion storing, in the storage portion, data of the status-indicating signal in association with time series data indicative of a series of time.... ... generating portion generating a status-indicating signal indicative of a status of the image forming portion;a storage portion storing data of the status-indicating signal; anda storage control portion storing, in the storage portion, data of the status-indicating signal in association with time series data indicative of a series of time.... ... the status-indicating signal, a type of an error that occurs in the image forming portion;a storage portion in which data of the status-indicating signal and data of the type of the error in association with time interval data indicative of a series of time are stored; anda storage control portion that controls storing, in the storage portion, data of the status-indicating signal and data of the type of the error in association with time interval data indicative of a series of time.>

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15/5,K/9 (Item 7 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0009774377 Drawing available

WPI Acc no: 2000-062050/200005

Related WPI Acc No: 1998-272626; 2000-052828; 2000-062049; 2000-062105

XRPX Acc No: N2000-048606

**Video data storage management system in digital video recorder for closed circuit**

**video security surveillance system**

Patent Assignee: SENSORMATIC ELECTRONICS CORP (SENS-N)

Inventor: LIN-LIU S; MACCORMACK D R; WINTER G J

Patent Family ( 8 patents, 83 countries )

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1999056216	A1	19991104	WO 1999US9317	A	19990429	200005	B
US 5996023	A	19991130	US 1996740627	A	19961031	200005	E
			US 199869257	A	19980429		
AU 199936706	A	19991116	AU 199936706	A	19990429	200015	E
BR 199909922	A	20001226	BR 19999922	A	19990429	200103	E
			WO 1999US9317	A	19990429		
EP 1073964	A1	20010207	EP 1999918899	A	19990429	200109	E
			WO 1999US9317	A	19990429		
JP 2002513224	W	20020508	WO 1999US9317	A	19990429	200234	E
			JP 2000546311	A	19990429		
AU 755668	B	20021219	AU 199936706	A	19990429	200312	E
CA 2325635	C	20060718	CA 2325635	A	19990429	200649	E
			WO 1999US9317	A	19990429		

Priority Applications (no., kind, date): US 1996740627 A 19961031; US 199869257 A 19980429

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
WO 1999056216	A1	EN	27	8		
National Designated States,Original	AL AM AT AU AZ BA BB BG BR BY CA CH CN CU CZ DE DK EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MD MG MK MN MW MX NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT UA UG UZ VN YU ZA ZW					
Regional Designated States,Original	AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW NL OA PT SD SE SL SZ UG ZW					
US 5996023	A	EN			C-I-P of application	US 1996740627
					C-I-P of patent	US 5884042
AU 199936706	A	EN			Based on OPI patent	WO 1999056216
BR 199909922	A	PT			PCT Application	WO 1999US9317
					Based on OPI patent	WO 1999056216
EP 1073964	A1	EN			PCT Application	WO 1999US9317
					Based on OPI patent	WO 1999056216
Regional Designated States,Original	DE FR GB SE					
JP 2002513224	W	JA	31		PCT Application	WO 1999US9317
					Based on OPI patent	WO 1999056216
AU 755668	B	EN			Previously issued patent	AU 9936706
					Based on OPI patent	WO 1999056216
CA 2325635	C	EN			PCT Application	WO 1999US9317
					Based on OPI patent	WO 1999056216

#### Alerting Abstract WO A1

NOVELTY - A hard disc in the recorder is divided into two areas, one a ring buffer for selectively storing video data signals which may be overwritten and other a main memory for storing signals for transfer to a digital tape. Based on alarm condition, a processor (44) may transfer data from buffer to main **memory** or assign a **portion** of disc as main **memory**.

DESCRIPTION - A multiplexer in front end electric circuit (42) is connected to video cameras for converting signals from video camera to video data signals. The alarm condition detector which includes an alarm sensor, analyzes video signal from the cameras for determining the alarm condition. An INDEPENDENT CLAIM is also included for a video data management method.



USE - In digital video recorder for closed circuit video security surveillance system.

ADVANTAGE - Efficient usage of limited disc **storage** space is enabled by assigning a portion of **storage** space as **ring buffer** . Unnecessary data are not stored permanently in the disc, thus enabling permanent recording of useful data. Hence, data storage efficiency is improved.

DESCRIPTION OF DRAWINGS - The figure shows the block diagram of digital video recorder.

42 Front end electric circuit

44 Processor

**Title Terms** /Index Terms/Additional Words: VIDEO; DATA; STORAGE; MANAGEMENT; SYSTEM; DIGITAL; RECORD; CLOSE; CIRCUIT; SECURE; SURVEILLANCE

**Class Codes**

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
H04N-005/76			Main		"Version 7"
G06F-0017/30	A	I		R	20060101
G08B-0013/196	A	I	L	B	20060101
H04N-0005/445	A	N		R	20060101
H04N-0005/76	A	I	F	B	20060101
H04N-0005/76	A	I	F	R	20060101
H04N-0005/76	A	I		R	20060101
H04N-0005/781	A	N		R	20060101
H04N-0005/782	A	N		R	20060101
H04N-0005/783	A	N		R	20060101
H04N-0005/907	A	I	L	B	20060101
H04N-0005/915	A	I	L	R	20060101
H04N-0005/92	A	I	L	R	20060101
H04N-0005/92	A	I		R	20060101
H04N-0007/18	A	I	L	R	20060101
H04N-0007/18	A	I		R	20060101
H04N-0009/804	A	N		R	20060101
H04N-0009/82	A	I		R	20060101
G06F-0017/30	C	I		R	20060101
G08B-0013/194	C	I	L	B	20060101
H04N-0005/445	C	N		R	20060101
H04N-0005/76	C	I	F	R	20060101
H04N-0005/76	C	I		R	20060101
H04N-0005/781	C	N		R	20060101
H04N-0005/782	C	N		R	20060101
H04N-0005/783	C	N		R	20060101
H04N-0005/907	C	I	L	B	20060101
H04N-0005/915	C	I	L	R	20060101
H04N-0005/92	C	I	L	R	20060101
H04N-0005/92	C	I		R	20060101
H04N-0007/18	C	I	L	R	20060101
H04N-0007/18	C	I		R	20060101
H04N-0009/804	C	N		R	20060101
H04N-0009/82	C	I		R	20060101

**ECLA:** G06F-017/30E, H04N-005/76, H04N-005/92N4, H04N-007/18C, H04N-009/82N4

**ICO:** T04N-005:445, T04N-005:781, T04N-005:782, T04N-005:783, T04N-009:804B, T04N-009:82N, T04N-009:82N4

**US Classification, Current Main:** 709-253000; **Secondary:** 348-E05099, 348-E07086, 358-538000, 358-539000 , 375-240010, 386-E05001, 386-E09040, 707-E17009, 709-231000

**US Classification, Issued:** 348394, 358538, 358539, 709231, 709253

Japan National Classification FI Terms

FI Term	Facet	Rank	Type
H04N-005/76 Z			
H04N-005/91 K			
H04N-007/18 D			

Japan National Classification F Terms

Theme	ViewPoint + Figure	Additional Code
5C052		
5C053		
5C054		
5C052	AA01	
5C052	AB03	
5C052	AB04	
5C052	AC08	
5C052	CC06	
5C052	CC11	
5C054	CG07	
5C054	EA01	
5C054	EA07	
5C053	FA12	
5C053	FA23	
5C054	GD06	
5C054	HA18	
5C053	HA29	
5C053	JA16	
5C053	LA01	
5C053	LA04	

File Segment: EPI;

DWPI Class: T01; W02; W04; W05

Manual Codes (EPI/S-X): T01-C07C2; T01-C08; T01-J05B2A; T01-J10A2; T01-J10E;  
W02-F01A5; W04-B01A; W04-B14C3; W05-B01C5

**Alerting Abstract** ...tape. Based on alarm condition, a processor (44) may transfer data from buffer to main **memory** or assign a **portion** of disc as main **memory**.  
...ADVANTAGE - Efficient usage of limited disc **storage** space is enabled by assigning a portion of **storage** space as **ring buffer**. Unnecessary data are not stored permanently in the disc, thus enabling permanent recording of useful... Original Publication Data by AuthorityArgentina**Publication No. Original Abstracts:** A hard disk (100) in a video data **storage** apparatus (20) includes a **first section** in which video **data** is stored and appointed for permanent recording (216), and a second section in which the video data is stored subject to rewriting after a short **period of time**. If an alarm event occurs (204), some or all of the data in the second **section** may be secured for permanent **storage** (216). The process to be employed in securing the data in the second **section** for permanent **storage** (216) is adapted to the proportion of the data in the second section which is desired to be secured... A hard disk in a video data **storage** apparatus includes a **first section** in which video **data** is stored and appointed for permanent recording, and a second section in which the video data is stored subject to rewriting after a short **period of time**. If an alarm event occurs, some or all of the data in the second **section** may be secured for permanent **storage**. The process to be employed in securing the **data** in the second **section** for permanent **storage** is adapted to the proportion of the data in the second **section** which is desired to be secured... A hard disk (100) in a video data storage apparatus (20) includes a **first section** in which video data is stored and **appointed** for permanent recording (216), and a **second section** in which the video data is stored subject to rewriting after a short **period of time**. If an alarm event occurs (204), some or all of the **data** in the second **section** may be secured for permanent **storage** (216). The process to be employed in **securing** the data in the second **section** for permanent **storage** (216) is adapted to the proportion of the data in the second **section** **which** is desired to be secured... **Claims:**data storage apparatus according to claim 1, wherein: said circular buffer means and said main **storage** means comprise, respectively, **first** and second **portions** of a hard disk drive; said first process comprises reading said stream of video data signals to be transferred **from** said circular buffer means and writing said read stream of video data signals to said... second process comprises designating said first portion of said hard disk drive to be a **portion** of said **main storage** means.

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15/5,K/10 (Item 8 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0008769381 *Drawing available*

WPI Acc no: 1998-312688/199827

Related WPI Acc No: 1995-132827; 1996-393662

XRFX Acc No: N1998-245038

**Data buffer for multiple data memory arrays in e.g. video server - in which data received in form of short duration data bursts is buffered to form long duration data segments, which are longer than short duration data bursts**

Patent Assignee: KONINK PHILIPS ELECTRONICS NV (PHIG); PHILIPS ELECTRONICS NORTH AMERICA CORP (PHIG); PHILIPS ELECTRONICS NV (PHIG); PHILIPS NORDEN AB (PHIG)

Inventor: LUCHT H; LUCHT P H

Patent Family ( 7 patents, 18 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 1998022868	A1	19980528	WO 1997IB1342	A	19971027	199827	B
EP 880736	A1	19981202	EP 1997945035	A	19971027	199901	E
			WO 1997IB1342	A	19971027		
US 5878280	A	19990302	US 1993125996	A	19930923	199916	E
			US 1995389672	A	19950216		
			US 1996752864	A	19961120		
JP 2000507017	W	20000606	WO 1997IB1342	A	19971027	200035	E
			JP 1998523389	A	19971027		
EP 880736	B1	20041201	EP 1997945035	A	19971027	200479	E
			WO 1997IB1342	A	19971027		
DE 69731822	E	20050105	DE 69731822	A	19971027	200505	E
			EP 1997945035	A	19971027		
			WO 1997IB1342	A	19971027		
DE 69731822	T2	20051201	DE 69731822	A	19971027	200579	E
			EP 1997945035	A	19971027		
			WO 1997IB1342	A	19971027		

Priority Applications (no., kind, date): US 1993125996 A 19930923; US 1995389672 A 19950216; US 1996752864 A 19961120

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
WO 1998022868	A1	EN	21	4		
National Designated States,Original	JP					
Regional Designated States,Original	AT BE CH DE DK ES FI FR GB GR IE IT LU MC NL PT SE					
EP 880736	A1	EN			PCT Application	WO 1997IB1342
					Based on OPI patent	WO 1998022868
Regional Designated States,Original	DE FR GB					
US 5878280	A	EN			C-I-P of application	US 1993125996
					C-I-P of application	US 1995389672
					C-I-P of patent	US 5539660
					C-I-P of patent	US 5671386
JP 2000507017	W	JA	29		PCT Application	WO 1997IB1342
					Based on OPI patent	WO 1998022868
EP 880736	B1	EN			PCT Application	WO 1997IB1342
					Based on OPI patent	WO 1998022868
Regional Designated States,Original	DE FR GB					
DE 69731822	E	DE			Application	EP 1997945035
					PCT Application	WO 1997IB1342
					Based on OPI patent	EP 880736
					Based on OPI patent	WO 1998022868
DE 69731822	T2	DE			Application	EP 1997945035
					PCT Application	WO 1997IB1342
					Based on OPI patent	EP 880736
					Based on OPI patent	WO 1998022868

### Alerting Abstract WO A1

The data buffer includes a number of input/output devices (11-16) coupled to a number of memory arrays (21-26) via a commutator (17). Within each memory array, a data buffer collects data for transfer to and/or from an array controller. Each memory array includes an error correction system for providing parity-based RAID type error correction. Disc controllers each having a FIFO buffer and respective disc memories are commonly coupled to a communications bus which in turn is coupled to the error correction system

by a FIFO buffer.

A segment buffer configured to provide ring buffers is coupled to the data bus. An access logic circuit controls the data flow through FIFO buffer and a CPU provides address sequence on the communications bus, for controlling data transfer to and from the segment buffer. An arbitrator resolves contentions between the FIFO buffer, the CPU and the disc controllers for access to the segment buffer.

USE - In video server system.

ADVANTAGE - Maintains continuous, reliable and deterministic channel bandwidths. Minimises reduction of bandwidth caused by disc drive data location time.

**Title Terms /Index Terms/Additional Words:** DATA; BUFFER; MULTIPLE; MEMORY; ARRAY; VIDEO ; SERVE; RECEIVE; FORM; SHORT; DURATION; BURST; LONG; SEGMENT

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G06F-003/06			Main		"Version 7"
G06F-0013/40	A	I		R	20060101
G06F-0003/06	A	I	F	R	20060101
G06F-0003/06	A	I		R	20060101
G11B-0027/00	A	I		R	20060101
H04N-0007/173	A	I		R	20060101
H04N-0007/24	A	I		R	20060101
G06F-0013/40	C	I		R	20060101
G06F-0003/06	C	I	F	R	20060101
G06F-0003/06	C	I		R	20060101
G11B-0027/00	C	I		R	20060101
H04N-0007/173	C	I		R	20060101
H04N-0007/24	C	I		R	20060101

**ECLA:** G06F-003/06M, G06F-013/40D2, G11B-027/00A, H04N-007/173B2, H04N-007/173B4, H04N-007/24

**US Classification, Issued:** 395872

Japan National Classification FI Terms			
FI Term	Facet	Rank	Type
G06F-003/06 540			

# Japan National Classification F Terms

Theme	ViewPoint + Figure	Additional Code
5B065		
5B065	BA01	
5B065	CA11	
5B065	CA30	
5B065	CE11	
5B065	EA03	

File Segment: EPI;  
 DWPI Class: T01; W02; W04  
 Manual Codes (EPI/S-X): T01-C01;  
 T01-H01A; W02-F10A; W02-F10K;  
 W04-F01M; W04-K05A

Original Publication Data by  
 AuthorityArgentina**Publication No.**  
 ...**Original Abstracts**:which in turn is



coupled to the error correction system by a FIFO buffer. A **segment** buffer having a plurality of low cost **memory** devices which are **configured** to provide a plurality of **ring buffers** is coupled to the data bus. An access logic circuit controls the data flow through the FIFO buffer and... .. which in turn is coupled to the error correction system by a FIFO buffer. A **segment** buffer having a plurality of low cost **memory** devices which are configured to provide a plurality of **ring buffers** is coupled to the data bus. An **access logic** circuit controls the data flow through the FIFO buffer and a central processing unit is... .. which in turn is coupled to the error correction system by a FIFO buffer. A **segment** buffer having a plurality of low cost **memory** devices which are configured to **provide** a plurality of **ring buffers** is coupled to the data bus. An access logic circuit controls the **data flow** through the FIFO buffer and a central processing unit is operative to provide address sequence... ..**Claims:**array controllers, each having a plurality of disk memories, said disk memories exhibiting data location **time intervals** when storing and/or retrieving data; andtransfer means (97-99) co-operating with said... .. data to and/or from said disk arrays; **characterized in that** the system (10) further **comprises** a plurality of buffer memories (80) providing a buffer memory for each of said disk controller... .. of short duration data bursts and accumulating said short duration data bursts to form data **segments** substantially larger than said data bursts and substantially greater in **duration** than said **time intervals** and providing said data segments to said arrays controllers, andfor receiving data from said... .. such data to said input/output devices in the form of said short duration data **bursts**; and that the **coupling** means is arranged for coupling data between said input/output devices and said plurality of... of short duration data bursts;transferring data to and/or from a plurality of disk **memories** in the form of data **segments** substantially larger in duration than said short duration data bursts;segment buffering data transferred from... .. said disk memories to accumulate said short duration data bursts into said data segments; and**segment** buffering data transferred from said disk **memories** to said input/output channels to form said data segments into said short duration data

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15/5,K/11 (Item 9 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0007088781 *Drawing available*

WPI Acc no: 1995-114648/199515

IRPX Acc No: N1995-090538

**Infusion apparatus for storing and infusing fluids - has accumulator gathering data on fluid flow, buffer storing data and accessor unit reading out data for display on user request**

Patent Assignee: BAXTER INT INC (BAXT); SHARP KK (SHAF)

Inventor: BENNETT C A; CALLAGHAN T; ESCHE G M; KAWAHARA M

Patent Family ( 2 patents, 2 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5395321	A	19950307	US 199342005	A	19930402	199515	B
JP 3138052	B2	20010226	JP 199281976	A	19920403	200114	E

Priority Applications (no., kind, date): JP 199281976 A 19920403

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
US 5395321	A	EN	14	9		
JP 3138052	B2	JA	10		Previously issued patent	JP 05277180

### Alerting Abstract US A

The apparatus has a peristaltic pump mechanism (8) pumping infusion fluid through an administration tube to a patient. A clock (26) counts a predetermined unit time. An accumulator gathers data representing a unit flow of the infusion fluid delivered by the pump. An accumulated infusion fluid volume is measured each time the clock counts a predetermined time.

A ring buffer (19) stores numerous accumulated infusion fluid volumes measured by the accumulator. Every volume is written in sequence to the memory each time the clock counts the unit time. An accessor unit reads out any of the stored accumulated fluid volumes at the request of an operator. A display (3) displays the accumulated fluid volume read out by the accessor. An input unit (4) allows input if the request.

ADVANTAGE - Removes need for manual recording of infused fluid volumes. Removes recording errors. Large time saving for doctor.

**Title Terms /Index Terms/Additional Words:** INFUSION; APPARATUS; STORAGE; FLUID; ACCUMULATOR; GATHER; DATA; FLOW; BUFFER; UNIT; READ; DISPLAY; USER; REQUEST

### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
A61M-0005/00	A	I	F	R	20060101
A61M-0005/172	A	I		R	20060101
A61M-0005/00	C	I	F	R	20060101
A61M-0005/168	C	I		R	20060101

**ECLA:** A61M-005/172

**US Classification, Issued:** 60467, 604153, 128DIG.013

**File Segment:** EngPI; EPI;

DWPI Class: S05; T01; P34

Manual Codes (EPI/S-X): S05-J01; T01-J06A

Original Publication Data by AuthorityArgentina**Publication No. ...Original Abstracts:**time. Data as to the current accumulated infusion fluid volume is stored in a second **storage section** of a **RAM**. **Each** time the **clock** counts the unit time, data representing the accumulated infusion fluid volume stored in the second **storage section** is written to a **ring buffer** in the **RAM**, so that the **ring buffer** stores data **representing** a plurality of accumulated infusion fluid volumes in one-to-one correspondence with a plurality of...  
...**Claims:**measured by said accumulating means, each accumulated infusion fluid volume being written in sequence into said storage means each **time** said clock means counts said predetermined unit time; accessing means for reading out any one...

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15/5,K/12 (Item 10 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0006354319 *Drawing available*

WPI Acc no: 1993-151938/199318

XRPX Acc No: N1993-116313

**TV conference system for automatically following speaker with camera - includes microphone input device deciding on presence of input for each speaker, buffers store decision result, unit specifies current speaker and driver for moving camera**  
Patent Assignee: FUJITSU LTD (FUIT)

Inventor: ASHIDA Y; HOMMA T; IMAI R; ISHIGURO H; NATORI H; SATO H; YAMAGUCHI M; YOSHIDA Y

Patent Family ( 1 patents, 1 countries )

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 5206721	A	19930427	US 1990490469	A	19900308	199318	B

Priority Applications (no., kind, date): US 1990490469 A 19900308

Patent Details

Patent Number	Kind	Lan	Pgs	Draw	Filing Notes
US 5206721	A	EN	57	36	

### Alerting Abstract US A

The television conference system includes a microphone input judgement unit for determining the existence of any input into microphones provided for the speakers of a television conference. A judgement result holding unit stores the results of the judgement

of existence of the microphone inputs over a predetermined period. This predetermined period is longer than the shortest period of continuous speaking. A time totaling unit obtains the total time of the microphone inputs during the predetermined period from the results of judgement held in the judgement result holding unit.

A speaker specifying unit is provided for specifying as the current speaker the speaker using a microphone having a total time obtained by the time totaling unit which is over a second predetermined period. This second predetermined period is the standard continuing period of noise and a camera drive unit ensures that the cammera is within a shooting range of the speaker specified as the current speaker by the speaker specifying unit.

**ADVANTAGE** - High precision specification of speakers, free from noise. Displays moving image even in multiple image transmission. Simultaneous display of participants and speaker.

**Title Terms /Index Terms/Additional Words:** TELEVISION; CONFER; SYSTEM; AUTOMATIC; FOLLOW; SPEAKER; CAMERA; MICROPHONE; INPUT; DEVICE; DECIDE; PRESENCE; BUFFER; STORAGE; RESULT; UNIT; SPECIFIED; CURRENT; DRIVE; MOVE

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
H04N-0007/00	A	I		R	20060101
H04N-0007/15	A	I		R	20060101
H04N-0007/00	C	I		R	20060101
H04N-0007/15	C	I		R	20060101

**ECLA:** H04N-007/00B, H04N-007/15

**US Classification, Current** Main: 348-014100; Secondary: 348-E07083, 348-E07091, 379-202010

**US Classification, Issued:** 35885, 37954, 379202, 379206

File Segment: EPI;

DWPI Class: W02; W04; W06

Manual Codes (EPI/S-X): W02-F08A3; W04-M01D2C; W06-A02E

Original Publication Data by Authority/Argentina**Publication No. ...Original**

**Abstracts:**predetermined period, the first predetermined period being longer than the shortest period of continuous speaking; a **time** totaling unit for obtaining the total time of the microphone inputs during the first predetermined... **...Claims:**as the number of samplings in the first predetermined period, and the storage buffers being **ring shift registers**; **time totaling** means for obtaining a total time of the microphone inputs during the first predetermined period...

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15/5,K/13 (Item 11 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0006221067 *Drawing available*

WPI Acc no: 1993-010662/199302

XRPX Acc No: N1993-008023

**Track jumping for optical disc players - transferring data to ring buffer and circuits to jump back one track to aid error recovery or avoid buffer overflow**

Patent Assignee: SONY CORP (SONY)

Inventor: AKIYAMA Y; KOYANAGI H; YONEMITSU; YONEMITSU J

Patent Family ( 13 patents, 6 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 522853	A2	19930113	EP 1992306305	A	19920709	199302	B
JP 5161115	A	19930625	JP 199292223	A	19920318	199330	E
NL 199300059	A	19931018	NL 199359	A	19930112	199345	E
EP 522853	A3	19930519	EP 1992306305	A	19920709	199403	E
AU 199331139	A	19940721	AU 199331139	A	19930111	199432	NCE
AU 662377	B	19950831	AU 199331139	A	19930111	199543	NCE
US 5471450	A	19951128	US 1992908164	A	19920702	199602	E
			US 1994350385	A	19941208		
EP 522853	B1	19971008	EP 1992306305	A	19920709	199745	E
DE 69222576	E	19971113	DE 69222576	A	19920709	199751	E
			EP 1992306305	A	19920709		
KR 245155	B1	20000302	KR 199212032	A	19920707	200122	E
JP 3158370	B2	20010423	JP 199292223	A	19920318	200125	E
NL 194880	B	20030106	NL 199359	A	19930112	200312	NCE
NL 194880	C	20030506	NL 199359	A	19930112	200360	NCE

Priority Applications (no., kind, date): JP 1991198591 A 19910712; JP 199292223 A 19920318; AU 199331139 A 19930111; NL 199359 A 19930112

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
EP 522853	A2	EN	23	14		
Regional Designated States,Original	DE FR GB					
EP 522853	A3	EN				
AU 662377	B	EN			Previously issued patent	AU 9331139
US 5471450	A	EN	20	14	Continuation of application	US 1992908164
EP 522853	B1	EN	23	0		
Regional Designated States,Original	DE FR GB					
DE 69222576	E	DE			Application	EP 1992306305
					Based on OPI patent	EP 522853
JP 3158370	B2	JA	17		Previously issued patent	JP 05161115

#### Alerting Abstract EP A2

The optical disc player includes a pick-up for obtaining data from a disc (16) and a **ring buffer memory** (45) for transferring and storing the data. Data is read from the buffer into a video code buffer (21) of a decoder (31). The data is decoded and displayed (29). A track jumping circuit (47) monitors various signals, including pending ring buffer overflow, and can jump the optical head back on track. In this case, when a backward jump occurs, no data is written into the buffer until the same sector number is found. Alternatively, if an error occurs, the backward jump is used to re-read the erroneous data. ADVANTAGE - Provides improved error recovery and buffer control.

**Title Terms /Index Terms/Additional Words:** TRACK; JUMP; OPTICAL; DISC; PLAY; TRANSFER; DATA; RING; BUFFER; CIRCUIT; BACK; ONE; AID; ERROR; RECOVER; AVOID; OVERFLOW

#### Class Codes

International Patent Classification						
IPC	Class Level	Scope	Position	Status	Version Date	
G11B-020/10; G11B-005/09; G11B-007/00; H04N-005/85; H04N-005/92; H04N-005/93; H04N-005/937			Main		"Version 7"	
G06F-015/62; G06F-015/68; G11B-021/08; H04N-009/80			Secondary		"Version 7"	

**ECLA:** G11B-020/10C, H04N-005/926B3, H04N-005/937, H04N-007/32B, H04N-007/52A

**ICO:** T04N-005:85

**US Classification, Issued:** 36960, 36950, 36954, 36958

Japan National Classification FI Terms			
FI Term	Facet	Rank	Type
G11B-020/18 532 H			
G11B-020/18 552 F			
G11B-020/18 572 C			
G11B-020/18 572 F			
G11B-020/18 574 B			
G11B-020/18 576 A			
G11B-020/18 576 F			
G11B-007/00 R			
G11B-007/005 Z			
H04N-005/85 A			
H04N-005/85 D			
H04N-005/85 Z			
H04N-005/93 C			

## Japan National Classification F Terms

Theme	ViewPoint + Figure	Additional Code
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5C052	
5C053	
5D090	
5D116	
5C052	AA01
5D090	AA01
5C052	AA02
5C052	AA03
5C052	AA17
5C052	AB04
5C052	AC01
5D090	BB02
5D090	BB03
5C052	BB04
5D090	BB04
5C052	BB07
5C053	CA11
5C052	CC01
5D090	CC04
5C052	CC06
5C052	CC12
5D090	CC16
5C052	CC20
5D090	DD03
5C052	DD08
5C052	DD09
5C052	DD10
5C052	EE06
5C053	FA23
5C053	FA24
5D090	FF25
5D090	FF30
5D090	FF43
5C053	GA11
5C052	GB01
5C053	GB14
5C053	GB17
5C053	GB37
5C053	GB40
5C052	GE04
5C052	GE05



File Segment: EPI;

DWPI Class: W04

Manual Codes (EPI/S-X): W04-C10A3; W04-F01F; W04-F01M

**Alerting Abstract** ...disc player includes a pick-up for obtaining data from a disc (16) and a **ring buffer memory** (45) for transferring and storing the data. Data is read from the buffer into a... **Equivalent Alerting Abstract** ...disc player includes a pick-up for obtaining data from a disc (16) and a **ring buffer memory** (45) for transferring and storing the data. Data is read from the buffer into a... **Technology Focus Original Publication Data by Authority Argentina Publication No. Original Abstracts:** A digital data reproducing apparatus and method which allows picture reproduction for a long **period of time** without **deterioration in picture** quality is described. Data are reproduced from an optical disk (16) by a pickup (41) and are transferred to and stored into a **ring buffer memory** (45). **Data read out from the ring buffer memory (45) are transferred to** and stored into a video code buffer (21) of a decoder section (31). The data... ... A track jump judging circuit (47) produces, when the amount of data stored in the **ring buffer memory** (45) **reaches a predetermined** value set in advance, a track jumping instruction to cause the pickup (41) to perform... ... A digital data reproducing apparatus and method which allows reproduction for a long **period of time** without deterioration in picture quality. Data **are reproduced from** an optical disk by a pickup and are transferred to and stored in a **ring buffer** memory. Data read from the **ring buffer memory are transferred** to and stored into a **video code** buffer of an encoder section. The data stored in the video code buffer are encoded in the encoder section and... ... track jump back to a preceding track when the amount of data stored in the **ring buffer memory** reaches a predetermined value. ...**Claims:** comprising: pickup means (41) for picking up said digital data from said disk (16); first **storage** means (45) for storing therein a predetermined **portion** of said digital data picked up by said pickup means (41); decoding means (31) for decoding said digital data read out... ... disc player includes a pick-up for obtaining data from a disc (16) and a **ring buffer memory** (45) for transferring and storing the data. Data is **read from the** buffer into a video code buffer (21) of a decoder (31). The data is decoded...

16/5,K/1 (Item 1 from file: 350)  
 DIALOG(R)File 350: Derwent WPIX  
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0015947836 *Drawing available*  
 WPI Acc no: 2006-479503/200649  
 XRPX Acc No: N2006-389933

**Monitoring system for detecting earth faults in electrical power supply grid, has monitoring devices installed in several locations, in which each monitoring device transmits alarm when detected level of harmonics of electric power is high**

Patent Assignee: NESA AS (NESA-N); DONG ENERGY SALES & DISTRIBUTIONS AS (DONG-N)

Inventor: BJORN L N; BJORN L

Patent Family ( 7 patents, 112 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 2006053567	A2	20060526	WO 2005DK737	A	20051118	200649	B
US 20060170410	A1	20060803	US 2004629213	P	20041118	200651	E
			US 2005283504	A	20051118		
EP 1820034	A2	20070822	EP 2005803993	A	20051118	200757	E
			WO 2005DK737	A	20051118		
WO 2006053567	A3	20071101				200774	E
AU 2005306167	A1	20060526	AU 2005306167	A	20051118	200780	E
IN 200702619	P4	20070907	WO 2005DK737	A	20051118	200836	E
			IN 2007CN2619	A	20070618		
JP 2008521374	W	20080619	WO 2005DK737	A	20051118	200843	E
			JP 2007541689	A	20051118		

Priority Applications (no., kind, date): US 2004629213 P 20041118; US 2005283504 A 20051118

Patent Details					
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes
WO 2006053567	A2	EN	217	135	
National Designated States, Original	AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KM KN KP KR KZ LC LK LR LS LT LU LV LY MA MD MG MK MN MW MX MZ NA NG NI NO NZ OM PG PH PL PT RO RU SC SD SE SG SK SL SM SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW				
Regional Designated States, Original	AT BE BG BW CH CY CZ DE DK EA EE ES FI FR GB GH GM GR HU IE IS IT KE LS LT LU LV MC MW MZ NA NL OA PL PT RO SD SE SI SK SL SZ TR TZ UG ZM ZW				
US 20060170410	A1	EN			Related to Provisional US 2004629213
EP 1820034	A2	EN			PCT Application WO 2005DK737
					Based on OPI patent WO 2006053567
Regional Designated States, Original	AL AT BA BE BG CH CY CZ DE DK EE ES FI FR GB GR HR HU IE IS IT LI LT LU LV MC MK NL PL PT RO SE SI SK TR YU				
WO 2006053567	A3	EN			
National Designated States, Original	AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KM KN KP KR KZ LC LK LR LS LT LU LV LY MA MD MG MK MN MW MX MZ NA NG NI NO NZ OM PG PH PL PT RO RU SC SD SE SG SK SL SM SY TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW				
Regional Designated States, Original	AT BE BG BW CH CY CZ DE DK EA EE ES FI FR GB GH GM GR HU IE IS IT KE LS LT LU LV MC MW MZ NA NL OA PL PT RO SD SE SI SK SL SZ TR TZ UG ZM ZW				
AU 2005306167	A1	EN			Based on OPI patent WO 2006053567
IN 200702619	P4	EN			PCT Application WO 2005DK737
JP 2008521374	W	JA	128		PCT Application WO 2005DK737
					Based on OPI patent WO 2006053567

### Alerting Abstract WO A2

NOVELTY - The system includes several monitoring devices mounted in a number of monitoring locations. Each monitoring device includes a detector for detecting level of harmonics in electrical power. A processor compares detected level of harmonics with a harmonics reference value. A communication unit transmits an alarm if the detected level of harmonics is above the reference value for a specific **period of time**.

DESCRIPTION - The detector may be provided in the form of a Faraday optical current

sensor (10). The level of harmonics is detected in a specific frequency range. Each monitoring device also includes a **memory** for storing the harmonics reference value. The processor calculates absolute content of harmonics or relative increase in harmonics. INDEPENDENT CLAIMS are included for the following:

- a. Monitoring method; and
- b. Communication between the master unit and several monitoring units.

USE - For detecting earth faults in electrical power supply grid.

ADVANTAGE - The cost-effective, modular monitoring system is designed for remote supervision, control and **condition** monitoring of medium voltage (MV) and low voltage (LV) power distribution networks. The monitoring system is designed for installation in existing and new MV/LV stations, and provides a platform for future applications. Provides the control center operator with reliable information from the LV/LV stations, both under normal **conditions** and during outages. Ensures that the operator can use the system for remote operation of controllable breakers and switches for fast fault isolation and power restoration. Ensures improved versatility in which the system can be used in new and existing MV and LV stations of different layout, can be easily customized to the specific application due to its modular design, does not require any current or voltage measurement to be present in the station, has broad range of supervision and control functions for fast fault localization, fast power restoration, data collection for planning purposes and optimal everyday operation, and handles different MV grounding principles. Ensures ease of installation and use, and improved reliability. Ensures safe and fast interconnection of modules using integrated terminal blocks. Ensures provisions for remote **parameter** setting. The system can be installed on ring main units without interrupting service to customers. Space requirements for system modules and sensors are small to allow installation in compact stations with modern compact switchgear. All modules and sensors of the system are continually supervised and can issue a system alarm or service **request**. Detects **persistent** loss of communication or severe system failures using cyclic check-up calls. Provides essential annunciations which are displayed locally on the module or through human or man-machine interface. Ensures that no periodic inspection or re-calibration is needed. Ensures easy retrofit of serviceable components.

DESCRIPTION OF DRAWINGS - The figure shows the schematic cross-sectional view of the Faraday optical current sensor.

10 Faraday optical current sensor

12 Oblong housing

14,16 Ends of housing

40,42 Sensor lids

44 Wings

**Title Terms** /Index Terms/Additional Words: MONITOR; SYSTEM; DETECT; EARTH; FAULT; ELECTRIC; POWER; SUPPLY; GRID; DEVICE; INSTALLATION; LOCATE; TRANSMIT; ALARM; LEVEL; HARMONIC; HIGH

## Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G01R-031/02			Main		"Version 7"
G01R-0015/24	A	I	L	B	20060101
G01R-0031/00	A	I	F	B	20060101
G01R-0031/02	A	I	F	B	20060101
G01R-0035/02	A	I	L	B	20060101
H02H-0003/52	A	I	L	B	20060101
H02J-0013/00	A	I	F	B	20060101
H02J-0013/00	A	I	L	B	20060101
H04L-0012/40	A	I	L	B	20060101
G01R-0015/24	C	I	L	B	20060101
G01R-0031/00	C	I	F	B	20060101
G01R-0031/02	C	I	F	B	20060101
G01R-0031/02	C	I	L	B	20060101
G01R-0035/00	C	I	L	B	20060101
H02H-0003/50	C	I	L	B	20060101
H02J-0013/00	C	I	F	B	20060101
H02J-0013/00	C	I	L	B	20060101
H04L-0012/40	C	I	L	B	20060101

**ECLA:** G01R-015/24C2, G01R-019/25D, G01R-031/02C2

**US Classification, Current Main:** 324-096000

**US Classification, Issued:** 32496

File Segment: EPI;

DWPI Class: S01; W05; X12

Manual Codes (EPI/S-X): S01-D01D5; S01-D03C5; S01-G04A; S01-H02; W05-D05C; W05-D07F; W05-D08E; X12-H03A; X12-H04E

**Alerting Abstract** ...alarm if the detected level of harmonics is above the reference value for a specific **period of time**. ... of harmonics is detected in a specific frequency range.

Each monitoring device also includes a **memory** for storing the harmonics reference value. The processor calculates absolute content of harmonics or relative... ..

**ADVANTAGE** - The cost-effective, modular monitoring system is designed for remote supervision, control and **condition** monitoring of medium voltage (MV) and low voltage (LV) power distribution networks. The monitoring system... .. the control center operator with reliable information from the LV/LV stations, both under normal **conditions** and

during outages. Ensures that the operator can use the system for remote operation of... ... Ensures safe and fast interconnection of modules using integrated terminal blocks. Ensures provisions for remote **parameter** setting. The system can be installed on ring main units without interrupting service to customers... ... sensors of the system are continually supervised and can issue a system alarm or service **request**. Detects **persistent** loss of communication or severe system failures using cyclic check-up calls. Provides essential annunciations... Original Publication Data by AuthorityArgentinaPublication No. ...**Original Abstracts:**is preferably detected in a specific frequency range. The monitoring devices may further include a **memory** device for storing a harmonics reference value and may still further include a processor for... ... an alarm provided the level of harmonics is above the reference level for a specific **period of time**. ... ... is detected in a specific frequency range. Each of the monitoring devices further includes a **memory** for storing a harmonics reference value, a processor for comparing the detected level of harmonics... ... alarm if the detected level of harmonics is above the reference level for a specific **period of time**. Each of the detectors includes an optical sensor for detecting the harmonics by use of... ... is preferably detected in a specific frequency range. The monitoring devices may further include a **memory** device for storing a harmonics reference value and may still further include a processor for... ... an alarm provided the level of harmonics is above the reference level for a specific **period of time**. ... ... detecter le niveau d'harmonique de la puissance electrique. Le niveau d'harmonique est de **preference** detecte dans une gamme de frequence specifique. Les dispositifs de commande peuvent en outre comporter... ... de commande peut egalement comprendre un dispositif de communication destine a transmettre une alarme a **condition** que le niveau d'harmonique soit superieur au niveau de reference pour une periode de...**Claims:**power signal, said detected level of harmonics being detected in a specific frequency range;a **memory** device for storing a harmonics reference value;a processor for comparing said detected level of... ... alarm if said detected level of harmonics is above said reference level for a specific **period of time.**>

Dialog eLink: [Order File History](#)  
16/5,K/2 (Item 2 from file: 350)  
DIALOG(R)File 350: Derwent WPIX  
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0010743256 *Drawing available*  
WPI Acc no: 2001-356062/200137  
Related WPI Acc No: 2001-389906; 2002-339172; 2002-339185; 2002-546618; 2003-512531; 2003-606495; 2005-354610; 2005-724398; 2006-380118; 2007-611850; 2009-A90654  
**User device for use in a traffic management system for content distribution over a world wide area network in which a customer pays for the provided services**  
Patent Assignee: DAY R D (DAYR-I); GUPTA A K (GUPT-I); SPEEDERA NETWORKS INC (SPEE-N); SWILDENS E S (SWIL-I); AKAMAI TECHNOLOGIES INC (AKAM-N)  
Inventor: DAY R D; GUPTA A; GUPTA A K; SWILDENS E S

Patent Family ( 5 patents, 92 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
WO 2001039000	A1	20010531	WO 2000US32306	A	20001121	200137	B
AU 200118007	A	20010604	AU 200118007	A	20001121	200153	E
US 20020152309	A1	20021017	US 1999166906	P	19991122	200270	E
			US 2000644927	A	20000823		
			US 2002141194	A	20020507		
US 6484143	B1	20021119	US 1999166906	P	19991122	200280	E
			US 2000641746	A	20000818		
US 7502858	B2	20090310	US 2000644927	A	20000823	200922	E
			US 2002141194	A	20020507		

Priority Applications (no., kind, date): US 1999166906 P 19991122; US 2000640886 A 20000818; US 2000641746 A 20000818; US 2000644927 A 20000823; US 2002141194 A 20020507

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
WO 2001039000	A1	EN	49	6		
National Designated States, Original	AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW					
Regional Designated States, Original	AT BE CH CY DE DK EA ES FI FR GB GH GM GR IE IT KE LS LU MC MW MZ NL OA PT SD SE SL SZ TR TZ UG ZW					
AU 200118007	A	EN			Based on OPI patent	WO 2001039000
US 20020152309	A1	EN			Related to Provisional	US 1999166906
					Division of application	US 2000644927
					Division of patent	US 6405252
US 6484143	B1	EN			Related to Provisional	US 1999166906
US 7502858	B2	EN			Division of application	US 2000644927
					Division of patent	US 6405252

#### Alerting Abstract WO A1

NOVELTY - Customer origination sites (107,109) will benefit from shared load

balancing and traffic management and a local distributed network server (113) queries a traffic management system (105) for name resolutions of customer web sites and receives a response specifying the server best suited to handle a request and to receive a home page (131). The web site of the customer is then upgraded and the customer pays only for the services provided.

DESCRIPTION - AN INDEPENDENT CLAIM is included for a service method for traffic management and content distribution using a world wide network.

USE - Traffic management and content distribution over a world wide network.

ADVANTAGE - Improved transfer of information.

DESCRIPTION OF DRAWINGS - The drawing is a simplified system diagram

107,109 Customer sites

113 Distributed network server

105 Traffic management system

131 Home page

**Title Terms /Index Terms/Additional Words:** USER; DEVICE; TRAFFIC; MANAGEMENT; SYSTEM; CONTENT; DISTRIBUTE; WORLD; WIDE; AREA; NETWORK; CUSTOMER; PAY; SERVICE

#### Class Codes

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
H04L-0012/14	A	I		R	20060101
H04L-0012/24	A	I		R	20060101
H04L-0029/06	A	I		R	20060101
H04L-0029/08	A	I		R	20060101
H04L-0029/12	A	I		R	20060101
G06F-0015/173	A	I	F	B	20060101
H04L-0012/14	C	I		R	20060101
H04L-0012/24	C	I		R	20060101
H04L-0029/06	C	I		R	20060101
H04L-0029/08	C	I		R	20060101
H04L-0029/12	C	I		R	20060101
G06F-0015/16	C	I		B	20060101

**ECLA:** H04L-012/14, H04L-012/24C3, H04L-012/24C4, H04L-029/06, H04L-029/08A7, H04L-029/08N1, H04L-029/08N27C, H04L-029/08N5, H04L-029/08N9A, H04L-029/08N9R, H04L-029/12A

**ICO:** T04L-029:06C8A, T04L-029:08A7, T04L-029:08N9A, T04L-029:12A2

**US Classification, Current Main:** 709-225000; **Secondary:** 709-217000

**US Classification, Issued:** 709225, 709217, 7051, 709223, 709224, 709235, 713213,



709226

File Segment: EPI;

DWPI Class: T01

Manual Codes (EPI/S-X): T01-H05B2

Original Publication Data by AuthorityArgentina**Publication No. ...Original**

**Abstracts:**d'abonnement utilisant ce systeme. Le dispositif et le systeme de cette invention sont de **preference** mis en application au moyen d'une nouvelle combinaison de gestion (100) de trafic mondial... ..**Claims:**cache server load balancing means for performing load balancing among said Web cache servers for **static** Web page content **requests**.... .. Web page content or streaming media, for the customer;a computing device including a computer **memory** coupled to the global traffic management device; andan accounting module coupled to the computing... .. global traffic management device and the content delivery network for the usage based upon a **period time** frequency... .. belonging to a plurality of customers; performing load balancing among said Web cache servers for **static** Web page content **requests** from requesting users; wherein a customer pays a fee to a service for use the

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16/5,K/3 (Item 3 from file: 350)

DIALOG(R)File 350: Derwent WPIX

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0006576332 *Drawing available*

WPI Acc no: 1993-388290/199349

XRPX Acc No: N1993-299868

**Query mechanism for extracting selected information from append-only database continuously - stores client-specified instrumental monotone queries as procedures which can be called and time-stamps records appended to database**

Patent Assignee: XEROX CORP (XERO)

Inventor: GOLDBERG D; NICHOLS D A; OKI B M; TERRY D B

Patent Family ( 6 patents, 5 countries )

Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
EP 573200	A2	19931208	EP 1993304051	A	19930525	199349	B
EP 573200	A3	19940119	EP 1993304051	A	19930525	199517	E
US 5495600	A	19960227	US 1992892869	A	19920603	199614	E
EP 573200	B1	19991027	EP 1993304051	A	19930525	199950	E
DE 69326856	E	19991202	DE 69326856	A	19930525	200003	E
			EP 1993304051	A	19930525		
JP 3240212	B2	20011217	JP 1993126326	A	19930527	200203	E

Priority Applications (no., kind, date): US 1992892869 A 19920603

Patent Details						
Patent Number	Kind	Lang	Pgs	Draw	Filing Notes	
EP 573200	A2	EN	7	3		
Regional Designated States,Original	DE FR GB					
EP 573200	A3	EN				
US 5495600	A	EN	7			
EP 573200	B1	EN				
Regional Designated States,Original	DE FR GB					
DE 69326856	E	DE			Application	EP 1993304051
					Based on OPI patent	EP 573200
JP 3240212	B2	JA	7		Previously issued patent	JP 06052230

#### Alerting Abstract EP A2

The system has a database manager that is installed on a digital computer for constructing one or more databases of time-stamped records. The records are time-stamped with time of being appended to the database.

Client-specified incremental monotone queries are parameterised by a current time value and a recorded time value indicating the time of last execution, and stored. The queries are periodically called to execute each one over the database records having times stamps falling into the **time-span** between when the query was last seen and the current time. The results are returned to the users who specified the respective queries.

ADVANTAGE - Continuous **query** mechanism permits **persistent** filters to be implemented on append-only databases for extracting information. Queries may be arbitrarily complex.

**Title Terms** /Index Terms/Additional Words: QUERY; MECHANISM; EXTRACT; SELECT; INFORMATION; APPENDAGE; DATABASE; CONTINUOUS; **STORAGE**; CLIENT; SPECIFIED; INSTRUMENT; MONOTONIC; PROCEDURE; CAN; CALL; TIME; STAMP; RECORD

**Class Codes**

International Patent Classification					
IPC	Class Level	Scope	Position	Status	Version Date
G06F-017/30			Main		"Version 7"
G06F-0017/30	A	I		R	20060101
G06F-0017/30	C	I		R	20060101

ECLA: G06F-017/30C

US Classification, Issued: 395600, 364974.6, 364974, 364DIG.002

File Segment: EPI;

DWPI Class: T01

Manual Codes (EPI/S-X): T01-J05B4

**Alerting Abstract** ...called to execute each one over the database records having times stamps falling into the **time-span** between when the query was last seen and the current time. The results are returned... ..ADVANTAGE - Continuous **query** mechanism permits **persistent** filters to be implemented on append-only databases for extracting information. Queries may be arbitrarily... **Title Terms** .../Index Terms/Additional Words: **STORAGE**; **Class Codes** Original Publication Data by AuthorityArgentina**Publication No.** ...**Original Abstracts**:then is installed on the database as a stored procedure that takes two date/time **parameters** (hereinafter referred to as "time" **parameters**), one of which (tau) identifies the last time the procedure was executed, and the other of... .. stored query procedures,thereby periodically executing the incremental queries over database records that have timestamps **spanning successive time slots**. ... .. then is installed on the database as a stored procedure that takes two date/time **parameters** (hereinafter referred to as "time" **parameters**), one of **which** (tau) identifies the last time the procedure was executed, and the other of which (t... .. stored query procedures, thereby periodically executing the incremental queries over database records that have timestamps **spanning successive time slots**. ...**Claims**:queries (QI) to execute each query over the database records having timestamps falling into the **time span** between when the query was last run and the **current time**; and means for returning query results to the users specifying the respective queries... .. monotone queries (QM) into incremental queries (Q') by parametrizing said incremental queries by two time **parameters**, wherein the first time **parameter** (tau) identifies the last time the incremental query (Q') was executed and the second time **parameter** (t) identifies the current time; a storing means **for** storing client-means-specified **incremental queries** (Q') in said system as callable procedures, each of said procedures being parametrized by **said** two time **parameters**; means for periodically calling said procedures to execute each client-means-specified incremental query (Q') over the database records having timestamps falling into the **time span** between when the **query** was last run and the current time; and means for returning query results to the users specifying the respective queries.

17/3K/1 (Item 1 from file: 348)  
DIALOG(R)File 348: EUROPEAN PATENTS  
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02446766

**Positioning device, electronic instrument, program, and storage medium storing program**

Positionierungsvorrichtung, elektronisches Instrument, Programm und Speichermedium mit gespeichertem Programm

Dispositif de positionnement, instrument électronique, programme et programme pour stocker le support de stockage

**Patent Assignee:**

- **Seiko Epson Corporation;** (2132631)  
4-1, Nishishinjuku 2-Chome; Shinjuku-ku,Tokyo 163-0811; (JP)  
(Applicant designated States: all)

**Inventor:**

- **Kimura, Akira**  
c/o Seiko Epson Corp.3-5, Owa 3-chome; Suwa-shiNagano-ken 392-8502; (JP)

**Legal Representative:**

- **HOFFMANN EITLE (101511)**  
Patent- und Rechtsanwälte Arabellastrasse 4; 81925 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	1916536	A2	20080430	(Basic)
Application	EP	2007017466		20070906	
Priorities	JP	2006290819		20061026	

**Designated States:**

AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;  
FI; FR; GB; GR; HU; IE; IS; IT; LI; LT;  
LU; LV; MC; MT; NL; PL; PT; RO; SE; SI;  
SK; TR;

**Extended Designated States:**

AL; BA; HR; MK; RS;

International Classification (Version 8) IPC	Level	Value	Position	Status	Version	Action	Source	Office
G01S-0001/00	A	I	F	B	20060101	20080205	H	EP

**Abstract Word Count:** 116

**NOTE:** 1

**NOTE:** Figure number on first page: 1

Legal Status	Type	Pub. Date	Kind	Text
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**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	200818	1304
SPEC A		(English)	200818	8230
Total Word Count (Document A) 9534				
Total Word Count (Document B) 0				
Total Word Count (All Documents) 9534				

**Specification:** ...According to one aspect of the invention, there is provided a positioning device comprising: a **memory** which is a **ring buffer** which includes a **storage** area capable of storing three or more cycles of a signal corresponding to at least an inversion **time interval** of a navigation message and in which a signal received by an RF reception circuit section which receives a GPS satellite signal is sequentially stored while shifting a **storage** position; and a positioning **section** which reads the signal from the **storage** area while changing a signal read position, performs an acquisition and/or tracking process of... ..According to another aspect of the invention, there is provided a positioning device comprising: a **memory** which is a **ring buffer** in which a signal received by an RF reception circuit section which receives a GPS... ..According to one embodiment of the invention, there is provided a positioning device comprising: a **memory** which is a **ring buffer** which includes a **storage** area capable of storing three or more cycles of a signal corresponding to at least... ..reception circuit section which receives a GPS satellite signal is sequentially stored while shifting a **storage** position; and a positioning **section** which reads the signal from the **storage** area while changing a signal read position, performs an acquisition and/or tracking process of... ..embodiment of the invention, there is provided a program for a positioning device including a **memory** which is a **ring buffer** which includes a **storage** area capable of storing three or more cycles of a signal corresponding to at least... ..which can execute a program, the program causing the processor to function as a positioning **section** which reads the signal from the **storage** area while changing a signal read position, performs an acquisition and/or tracking process of... ..a specific positioning process.

According to the above configuration, the signal is read from the **storage** area of the **memory (ring buffer)** which can store three or more cycles of a signal while changing the signal read... ..preceding or subsequent to the specific-time signal set having the maximum strength;

a determination **section** which determines the read position from the **memory** used for the correlation process based on the position of the memory at which the... ..a specific positioning process.

According to the above configuration, the signal set for the specific **period of time** is acquired from the **memory (ring buffer)** while changing the acquisition timing, and the signal strength of each signal set is calculated...

**Claims:** ...or subsequent to the specific-time signal set having the maximum strength; and a determination **section** which determines the read position from the **memory** used for the correlation process based on the position of the memory at which the... ..one of claims 1 to 5.

7. A program for a positioning device including a **memory** which is a **ring buffer** which includes a **storage** area capable of storing three or more cycles of a signal corresponding to at least an inversion **time interval** of a navigation message and in which a signal received by an RF reception circuit... ..which can execute a program, the program causing the processor to function as a positioning **section** which reads the signal from the **storage** area while changing a signal read position, performs an acquisition and/or tracking process of...

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17/3K/2 (Item 2 from file: 348)  
DIALOG(R)File 348: EUROPEAN PATENTS  
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02169673

**MOVEMENT DETECTION DEVICE AND MOVEMENT DETECTION METHOD**  
**BEWEGUNGSDETEKTIONSEINRICHTUNG UND BEWEGUNGSDETEKTIONSVERFAHREN**  
**DETECTEUR DE MOUVEMENT ET PROCEDE DE DETECTION DE MOUVEMENT**

**Patent Assignee:**

- **International Business Machines Corporation;** (828995)  
New Orchard Road; Armonk, NY 10504; (US)  
(Applicant designated States: all)

**Inventor:**

- **SUGAWARA, Takashic/o Yamato site, IBM Japan Ltd.**  
1623-14, Shimotsuruma; Yamato-shi, Kanagawa 2428502; (JP)
- **KAWAHARA, Koujic/o Yamato site, IBM Japan Ltd.**  
1623-14, Shimotsuruma; Yamato-shi, Kanagawa 2428502; (JP)
- **CHOTOKU, Yujic/o Yamato site, IBM Japan Ltd.**  
1623-14, Shimotsuruma; Yamato-shi, Kanagawa 2428502; (JP)

**Legal Representative:**

- **Sekar, Anita (145211)**  
IBM United Kingdom Limited, Intellectual Property Department, Hursley Park;  
Winchester, Hampshire SO21 2JN; (GB)

	Country	Number	Kind	Date	
Patent	EP	1808679	A1	20070718	(Basic)
	WO	2006049158		20060511	
Application	EP	2005800516		20051101	
	WO	2005JP20095		20051101	
Priorities	JP	2004321532		20041105	

**Designated States:**

AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;  
FI; FR; GB; GR; HU; IE; IS; IT; LI; LT;  
LU; LV; MC; NL; PL; PT; RO; SE; SI; SK;  
TR;

**Extended Designated States:**

AL; BA; HR; MK; YU;

International Classification (Version 8) IPC	Level	Value	Position	Status	Version	Action	Source	Office
G01H-0017/00	A	I	F	B	20060101	20061026	H	EP
G01H-0001/00	A	I	L	B	20060101	20061026	H	EP

**Abstract** ...sensor 12 which detects acceleration generated by motion of an electronic device; a motion detecting section 18 including a statistical processing section which

calculates the average value of data provided from the acceleration sensor 12, calculates the...

**Abstract Word Count:** 209

**NOTE:** 001

**NOTE: Figure number on first page:** 001

Legal Status	Type	Pub. Date	Kind	Text
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**Language** Publication: English

**Procedural:** English

**Application:** Japanese

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	200729	1144
SPEC A		(English)	200729	5288
Total Word Count (Document A) 6432				
Total Word Count (Document B) 0				
Total Word Count (All Documents) 6432				

**Specification:** ...to the external module to control the response of the external module.

The motion detecting **section 18** of the present invention further includes a **statistical** processing **section 26** that performs **statistical** processing of an acceleration signal according to the present invention and a buffer memory consisting... ...the statistical processing. Once a pseudo-variance value V is generated as a result of **statistical** processing in the motion detecting **section 18**, the pseudo-variance value is sent to the signal generating section 20. In the... ...comparing them with a threshold motion value. The binary signal value generated in the comparing **section 30** is sent to a buffer **memory 32**, where signal values are sequentially accumulated at predetermined **time intervals**. The buffer memory 32 in the present invention may be any of various types of **memories** and preferably is a **ring buffer** for the purpose of sequentially accumulating data in a certain period.

The data in the buffer **memory 32** is added up in an accumulating **section 34** to obtain an accumulated value at every predetermined sampling interval. The accumulated value is... ...micro control code.

Figure 2 is a block diagram showing a functional configuration of the **statistical** processing **section 26** according to the present invention along with a configuration of the buffer memory 28. Specifically, the **statistical** processing **section 26** of the present invention includes a short-time motion deviation generating section 50 and... ...table or a truth table to determine a response.

Figure 4 is a flowchart of **statistical** processing performed by the control **section** of the present invention. The processing in the control section starts at step S100 and...digital



signal by an A/D converter 14 and is input in a motion detecting **section** 18, where the **statistical** processing described above is performed to generate a pseudo-variance value. The generated pseudo-variance... ..a signal generating section according to the present invention;

Figure 4 is a flowchart of **statistical** processing performed by a control **section** according to the present invention;

Figure 5 shows processing performed in the signal generating section... ..sensor, 14 ... A/D converter, 16 ... Control section, 18 ... Motion detecting section, 20 ... Signal generating **section**, 22 ... Internal bus line, 24 ... Interface, 26 ... **Statistical** processing **section**, 28 ... Buffer memory, 30 ... Threshold comparing **section**, 32 ... Buffer memory, 34 ... Accumulating section, 36 ... Control signal generating section, 40 ... X-direction averaging...

**Claims:** ...acceleration sensor which detects acceleration generated by motion of the electronic device; a motion detecting **section** including a **statistical** processing **section** which calculates the average value of data provided from the acceleration sensor, calculates the difference... ..at predetermined time intervals.

2. The motion detection apparatus according to claim 1, wherein the **statistical** processing **section** comprises: a short-time motion deviation generating **section** which generates a plurality of the differences from the average value of the data at...

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17/3K/3 (Item 3 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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02096916

### **Content reproduction system, content reproduction apparatus and content reproduction method**

Inhaltswiedergabesystem, Inhaltswiedergabevorrichtung und Inhaltswiedergabeverfahren  
Systeme, appareil et procede de reproduction de contenu

### **Patent Assignee:**

- **SONY CORPORATION;** (214022)  
7-35, Kitashinagawa 6-chome, Shinagawa-ku; Tokyo; (JP)  
(Applicant designated States: all)

### **Inventor:**

- **Michiaki, Yoneda, Sony Corporation**  
7-35, Kitashinagawa 6-chome Shinagawa-ku; Tokyo; (JP)

**Legal Representative:**

- **Merryweather, Colin Henry (83331)**  
J.A. Kemp & Co. 14 South Square Gray's Inn; London WC1R 5JJ; (GB)

	Country	Number	Kind	Date	
Patent	EP	1691557	A1	20060816	(Basic)
Application	EP	2006250404		20060125	
Priorities	JP	200533406		20050209	

**Designated States:**

AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;  
FI; FR; GB; GR; HU; IE; IS; IT; LI; LT;  
LU; LV; MC; NL; PL; PT; RO; SE; SI; SK;  
TR;

**Extended Designated States:**

AL; BA; HR; MK; YU;

International Classification (Version 8) IPC	Level	Value	Position	Status	Version	Action	Source	Office
H04N-0007/24	A	I	F	B	20060101	20060518	H	EP
G11B-0027/32	A	I	L	B	20060101	20060518	H	EP
G11B-0027/034	A	I	L	B	20060101	20060518	H	EP

**Abstract Word Count: 154**

**NOTE: 2**

**NOTE: Figure number on first page: 2**

Legal Status Type	Pub. Date	Kind	Text
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**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	200633	775
SPEC A		(English)	200633	3339
Total Word Count (Document A) 4114				
Total Word Count (Document B) 0				

Fulltext Availability	Available Text	Language	Update	Word Count
Total Word Count (All Documents) 4114				

**Specification:** ...content request section 32 determines the end of the music content based on the reproduction **time length** of the music content. The content request **section 32** compares the **storage** amount of the music content in the reception **ring buffer 33** and residual reproduction time of the currently reproduced track. The reception ring buffer 33...

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17/3K/4 (Item 4 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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02078552

**MEDICAL IMAGE PROCESSING METHOD**  
**MEDIZINISCHES BILDBEARBEITUNGSVERFAHREN**  
**PROCEDE DE TRAITEMENT D'IMAGE MEDICALE**

**Patent Assignee:**

- **Olympus Corporation;** (7680330)  
43-2, Hatagaya 2-chome Shibuya-ku; Tokyo 151-0072; (JP)  
(Applicant designated States: all)

**Inventor:**

- **NISHIMURA, Hirokazu,Olympus Corporation**  
43-2 Hatagaya 2-chome Shibuya-ku; Tokyo 152-0072; (JP)
- **HASEGAWA, JunOlympus Corporation**  
43-2 Hatagaya 2-chome Shibuya-ku; Tokyo 152-0072; (JP)
- **TANAKA, HidekiOlympus Corporation**  
43-2 Hatagaya 2-chome Shibuya-ku; Tokyo 1520072; (JP)
- **INOUE, RyokoOlympus Corporation**  
43-2 Hatagaya 2-chome Shibuya-ku; Tokyo 152 0072; (JP)
- **NONAMI, TetsuoOlympus Corporation**  
43-2 Hatagaya 2-chome Shibuya-ku; Tokyo 152-0072; (JP)

**Legal Representative:**

- **von Hellfeld, Axel (53042)**  
Wuesthoff & Wuesthoff Patent- und Rechtsanwälte Schweigerstrasse 2; 81541 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	1842481	A1	20071010	(Basic)
	WO	2006062163		20060615	
Application	EP	2005814386		20051208	
	WO	2005JP22562		20051208	
Priorities	JP	2004359054		20041210	
	JP	2004360319		20041213	

#### Designated States:

DE; FR; GB;

#### Extended Designated States:

AL; BA; HR; MK; YU;

International Classification (Version 8) IPC	Level	Value	Position	Status	Version	Action	Source	Office
A61B-0001/04	A	I	F	B	20060101	20060619	H	EP

Abstract Word Count: 75

NOTE: 1

NOTE: Figure number on first page: 1

Legal Status Type	Pub. Date	Kind	Text
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Language Publication: English

Procedural: English

Application: Japanese

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	200741	1207
SPEC A		(English)	200741	15186
Total Word Count (Document A) 16393				
Total Word Count (Document B) 0				
Total Word Count (All Documents) 16393				

**Specification:** ...and stored. In an example in Fig. 30, mucous information is stored sequentially in a **time series**, for example, of mucous information 162a, 162b, 162c, and 162d. When the mucous information is ... ..In Fig. 30, 4 pieces of mucous information 162a to 162d are shown, but the **ring buffer** may have a larger **memory capacity**.

The mucous feature value processing **portion** 160 determines presence of a Barrett's mucous in the image from the pixel number...

Dialog eLink: [Order File History](#)

17/3K/5 (Item 5 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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01971590

### **Ultrasound diagnostic apparatus**

Diagnostisches Ultraschallgerät

Dispositif d'ultrason de diagnostic

### **Patent Assignee:**

- **Aloka Co. Ltd.;** (445434)  
6-22-1 Mure; Mitaka-shi Tokyo, 181-8622; (JP)  
(Applicant designated States: all)

### **Inventor:**

- **Watanabe, Tetsuo**  
c/o ALOKA CO.,LTD. 6-22-1, Mure; Mitaka-shi Tokyo, 181-8622; (JP)
- **Ohtake, Akifumi**  
c/o ALOKA CO.,LTD. 6-22-1, Mure; Mitaka-shi Tokyo, 181-8622; (JP)
- **Uno, Takaya**  
c/o ALOKA CO.,LTD. 6-22-1, Mure; Mitaka-shi Tokyo, 181-8622; (JP)

### **Legal Representative:**

- **Heim, Hans-Karl et al (49352)**  
Weber & Heim Patentanwälte Irmgardstrasse 3; 81479 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	1589477	A1	20051026	(Basic)
Application	EP	2005007985		20050412	
Priorities	JP	2004125145		20040421	

### **Designated States:**

AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;

FI; FR; GB; GR; HU; IE; IS; IT; LI; LT;  
LU; MC; NL; PL; PT; RO; SE; SI; SK; TR;

**Extended Designated States:**

AL; BA; HR; LV; MK; YU;

**International Patent Class (V7):** G06T-001/60; A61B-008/00  
**Abstract Word Count:** 121

**NOTE:** 1

**NOTE:** Figure number on first page: 1

Legal Status	Type	Pub. Date	Kind	Text
--------------	------	-----------	------	------

**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	200543	671
SPEC A		(English)	200543	6582
Total Word Count (Document A) 7253				
Total Word Count (Document B) 0				
Total Word Count (All Documents) 7253				

**Specification:** ...an ultrasound diagnostic apparatus, and more particularly to a technique for storing data in a **memory section**. Description of Related Art

Most ultrasound (ultrasonic) diagnostic apparatuses used in the medical field include a cine-**memory** which is formed as a **ring buffer** or a temporary **storage**. A cine- **memory** stores data obtained through the transmission and reception of ultrasound. An ultrasound image is formed... ..memory generally has a storage capacity allowing storage of multiple numbers of frames obtained in **time sequence**. In a cine-memory, the newest input data overwrites the oldest data which is already... ..of memory devices, and temporarily stores the frame line output from the DSC 16 in **time sequence**. In a normal operation mode, as in the conventional art, the cine-**memory** 60 as a whole functions as a single **ring buffer**. Therefore, the cine- **memory** 60 continuously stores a series of frames starting from the frame which was stored at... ..in a stress echo test, prior to execution of the first capturing process, the cine-**memory** 60 is divided into a plurality of **segments** 62 (see Fig. 1) in accordance with the number of capturing processes, namely the number... ..divided into a plurality of storage sub-regions, each of which functions as a temporary **storage** or a **ring buffer**. Each **segment** 62 stores one or a plurality of frame blocks obtained in the corresponding capturing process... ..or a plurality of frame blocks obtained in each capturing process are stored in each **segment** 62 on the cine-**memory** 60.

More specifically, in a stress echo test, i stages are set and j views... ..memory. The frame line 104 is composed of a plurality of frames 102 arranged in **time sequence**. Indicator(C) shows the content of a single **segment 62** in the cine-**memory**. As described above, the **segment 62** constitutes a single **ring buffer** and stores each frame sequentially input in the order of address in the segment 62...

Dialog eLink: [Order File History](#)

17/3K/6 (Item 6 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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01241119

**Method of and apparatus for reproducing audio information and computer program therefor**

Verfahren, Vorrichtung und Rechnerprogramm zur Wiedergabe von Audioinformation

Procede, appareil et programme d'ordinateur de reproduction d'information audio

**Patent Assignee:**

- **Pioneer Corporation;** (2812420)  
4-1, Meguro 1-chome; Meguro-ku,Tokyo; (JP)  
(Proprietor designated states: all)

**Inventor:**

- **Yamada, Youichi**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Isobe, Hiroyuki**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Suzuki, Takashi**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Sakata, Koichiro**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Kimura, Tomohiko**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Funada, Takeaki**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)

- **Kamei, Kazuo**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Inoshita, Gen**  
Pioneer Corp., Ohmori Works,4-15-5 Ohmorinishi; Ohta-ku,Tokyo-to; (JP)
- **Chiba, Kensuke**  
Pioneer Corp., Ohmori Works,4-15-5 Ohmorinishi; Ohta-ku,Tokyo-to; (JP)
- **Amemiya, Naomi**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Eguchi, Hiroyasu**  
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- **Kaburagi, Keitaro**  
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- **Yagi, Akiharu**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Yada, Toru**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)
- **Kataoka, Yoshinori**  
Pioneer Corp., Tokorozawa Works.,4-2610 Hanazono; Tokorozawa-shi,Saitama-ken; (JP)

#### Legal Representative:

- **Reinhard - Skuhra - Weise & Partner (100731)**  
Postfach 44 01 51; 80750 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	1074990	A1	20010207	(Basic)
	EP	1074990	B1	20060531	
Application	EP	2000116223		20000804	
Priorities	JP	99221094		19990804	

#### Designated States:

DE; ES; FR; GB; IT; NL;

#### Extended Designated States:

AL; LT; LV; MK; RO; SI;

**International Patent Class (V7):** G11B-027/10; G11B-027/34; G11B-027/36; G10H-001/00; G11B-019/02



International Classification (Version 8) IPC	Level	Value	Position	Status	Version	Action	Source	Office
G11B-0027/10	A	I	F	B	20060101	20001123	H	EP
G11B-0027/34	A	I	L	B	20060101	20001123	H	EP
G11B-0027/36	A	I	L	B	20060101	20001123	H	EP
G10H-0001/00	A	I	L	B	20060101	20001123	H	EP
G11B-0019/02	A	I	L	B	20060101	20001123	H	EP

**Abstract Word Count:** 147

**NOTE:** 3

**NOTE:** Figure number on first page: 3

Legal Status Type	Pub. Date	Kind	Text
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**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability Available Text	Language	Update	Word Count
CLAIMS A	(English)	200106	1
SPEC A	(English)	200106	1
CLAIMS B	(English)	200622	1115
CLAIMS B	(German)	200622	1009
CLAIMS B	(French)	200622	1342
SPEC B	(English)	200622	11164
Total Word Count (Document A) 2			
Total Word Count (Document B) 14630			
Total Word Count (All Documents) 14632			

**Specification:** ...composition is detected from the optical disc DB in the player 3, and its head **portion** is stored into the cue **memory** 40 in the **memory** 23 (Step S21).

Next, **portions** of the next music composition after the head **portion** stored in the cue **memory** 40 are sequentially stored into the **ring buffer memory** 41, by searching the head **portion** of the next music composition again (Step S22). The BPM value of the next music composition being stored in the **ring buffer memory** 41 is detected by using the BPM detector 38 in the DSP 23, and is...detected at the step S23 or the BPM value cannot be detected and a predetermined **time period** has elapsed (a so-called "time-over" occurs) (Step S24). If the BPM value cannot...

Dialog eLink: [Order File History](#)

17/3K/7 (Item 7 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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00694339

**METHOD AND DEVICE FOR REPRODUCING DATA**  
**VERFAHREN UND VORRICHTUNG ZUR DATENWIEDERGABE**  
**PROCEDE ET APPAREIL DE REPRODUCTION DE DONNEES**

**Patent Assignee:**

- **SONY CORPORATION;** (214021)  
7-35 Kitashinagawa 6-chome Shinagawa-ku; Tokyo 141; (JP)  
(Proprietor designated states: all)

**Inventor:**

- **IWAMURA, Ryuichi**  
Sony Corporation 7-35, Kitashinagawa 6-chome; Shinagawa-ku Tokyo 141; (JP)
- **MASUDA, Shozo Sony Corporation**  
7-35, Kitashinagawa 6-chome; Shinagawa-ku Tokyo 141; (JP)
- **AKIYAMA, Yoshiyuki**  
Sony Corporation 7-35, Kitashinagawa 6-chome; Shinagawa-ku Tokyo 141; (JP)

**Legal Representative:**

- **DeVile, Jonathan Mark et al (91152)**  
D. Young & Co., 21 New Fetter Lane; London EC4A 1DA; (GB)

	Country	Number	Kind	Date	
Patent	EP	671738	A1	19950913	(Basic)
	EP	671738	A1	19960807	
	EP	671738	B1	20010711	
	WO	9509421		19950406	
Application	EP	94927795		19940928	
	WO	94JP1604		19940928	
Priorities	JP	93242532		19930929	

**Designated States:**

DE; FR; GB;

**International Patent Class (V7):** G11B-020/18; H03M-013/00; H04N-009/804  
**Abstract Word Count:** 172

Legal Status	Type	Pub. Date	Kind	Text
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**Language** Publication: English

Procedural: English

Application: Japanese

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	EPAB95	1363
SPEC A		(English)	EPAB95	10856
CLAIMS B		(English)	200128	831
CLAIMS B		(German)	200128	707
CLAIMS B		(French)	200128	1007
SPEC B		(English)	200128	9963
Total Word Count (Document A) 12220				
Total Word Count (Document B) 12508				
Total Word Count (All Documents) 24728				

**Specification:** ...codes exist when viewed from outer code direction is inner code flag.

Data on the **ring buffer memory** 5 in which error correction has been completed in this way is eventually read out to video code buffer **memory** 10 of decode **section** 20 at the **time period** Zr of FIG. 10. PZr of FIG. 11 is read pointer in this case. Data...

**Specification:** ...codes exist when viewed from outer code direction is inner code flag.

Data on the **ring buffer memory** 5 in which error correction has been completed in this way is eventually read out to video code buffer **memory** 10 of decode **section** 20 at the **time period** Zr of FIG. 10. PZr of FIG. 11 is read pointer in this case. Data...

Dialog eLink: [Order File History](#)

17/3K/8 (Item 8 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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00591027

**Communication system for automotive navigation system.**

Kommunikationssystem für Fahrzeugnavigationssystem.

Système de communication pour système de navigation automobile.

**Patent Assignee:**

- **PIONEER ELECTRONIC CORPORATION;** (537924)  
4-1, Meguro 1-chome; Meguro-ku Tokyo; (JP)  
(applicant designated states: DE;FR;GB)

**Inventor:**

- **Hatano, Ichirou, c/o Pioneer Electronic Corp.**  
4-1 Meguro 1-chome; Meguro-ku, Tokyo; (JP)
- **Shimizu, Toshihiko, c/o Pioneer Electronic Corp.**  
4-1 Meguro 1-chome; Meguro-ku, Tokyo; (JP)
- **Watanabe, Shuji, c/o Pioneer Electronic Corp.**  
4-1 Meguro 1-chome; Meguro-ku, Tokyo; (JP)
- **Kamioka, Yoshiyuki, c/o Pioneer Electronic Corp.**  
4-1 Meguro 1-chome; Meguro-ku, Tokyo; (JP)

**Legal Representative:**

- **MEISSNER, BOLTE & PARTNER (100193)**  
Patentanwalte Postfach 86 06 24; D-81633 Munchen; (DE)

	Country	Number	Kind	Date	
Patent	EP	585950	A2	19940309	(Basic)
	EP	585950	A3	19940629	
Application	EP	93114174		19930903	
Priorities	JP	92263099		19920904	

**Designated States:**

DE; FR; GB;

**International Patent Class (V7):** G01S-005/14; G01S-005/00; G01C-021/20; **Abstract Word Count:** 128

Legal Status Type	Pub. Date	Kind	Text
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Language Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	EPABF2	226
SPEC A		(English)	EPABF2	1824
Total Word Count (Document A) 2050				

Fulltext Availability	Available Text	Language	Update	Word Count
Total Word Count (Document B) 0				
Total Word Count (All Documents) 2050				

**Specification:** ...on a map shown on a display. Locations of the automobile are detected at regular **intervals of time or distance**, and stored in a **ring buffer memory** as a route which the automobile has taken to the present location. When the memory...

Dialog eLink: [Order File History](#)

17/3K/9 (Item 9 from file: 349)

DIALOG(R)File 349: PCT FULLTEXT

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01182635

## ENHANCED ACOUSTIC DETECTION OF GAS LEAKS IN UNDERGROUND GAS PIPELINES

DETECTION ACOUSTIQUE DE FUITES DE GAZ AMELIOREE DANS DES CONDUITES DE GAZ SOUTERRAINES

### Patent Applicant/Patent Assignee:

- GAS TECHNOLOGY INSTITUTE**; 1700 South Mount Prospect Road, Des Plaines, IL 60018  
 US; US(Residence); US(Nationality)

### Legal Representative:

- FEJER Mark E(agent)**  
 Gas Technology Institute, 1700 South Mount Prospect Road, Des Plaines, IL 60018; US;

	Country	Number	Kind	Date
Patent	WO	2004104570	A1	20041202
Application	WO	2004US11465		20040414
Priorities	US	2003439157		20030515

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)

AE; AG; AL; AM; AT; AU; AZ; BA; BB; BG;  
 BR; BW; BY; BZ; CA; CH; CN; CO; CR; CU;

CZ; DE; DK; DM; DZ; EC; EE; EG; ES; FI;  
GB; GD; GE; GH; GM; HR; HU; ID; IL; IN;  
IS; JP; KE; KG; KP; KR; KZ; LC; LK; LR;  
LS; LT; LU; LV; MA; MD; MG; MK; MN; MW;  
MX; MZ; NA; NI; NO; NZ; OM; PG; PH; PL;  
PT; RO; RU; SC; SD; SE; SG; SK; SL; SY;  
TJ; TM; TN; TR; TT; TZ; UA; UG; US; UZ;  
VC; VN; YU; ZA; ZM; ZW;

[EP] AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES;  
FI; FR; GB; GR; HU; IE; IT; LU; MC; NL;  
PL; PT; RO; SE; SI; SK; TR;

[OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;  
ML; MR; NE; SN; TD; TG;

[AP] BW; GH; GM; KE; LS; MW; MZ; SD; SL; SZ;  
TZ; UG; ZM; ZW;

[EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

**Language** Publication Language: English

Filing Language: English

Fulltext word count: 7361

### Detailed Description:

...have very little effect on the results. In addition, field experience has shown that the **time** span covered by the rms value is long enough to be representative of the acoustic...  
...effects.

Fig. 6 is a flow diagram showing the functions performed by the PC.

At **time intervals** greater than 128 digitization periods, the PC collects data and flags from the DSP. In that **length of time**, the **portion of the ring- buffer memories** used to calculate the rms values has been filled with new data so that all...

Dialog eLink: [Order File History](#)

17/3K/10 (Item 10 from file: 349)

DIALOG(R)File 349: PCT FULLTEXT

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00974210

**APPARATUS, METHOD AND SYSTEM WITH A GRAPHICS-RENDERING  
ENGINE HAVING A TIME ALLOCATOR**  
APPAREIL, PROCEDE ET SYSTEME A MOTEUR DE RENDU GRAPHIQUE  
EQUIPE D'UN ALLOCATEUR TEMPOREL

**Patent Applicant/Patent Assignee:**

- **INTEL CORPORATION**; 2200 Mission College Boulevard, Santa Clara, CA 95052  
US; US(Residence); US(Nationality)

**Legal Representative:**

- **MALLIE Michael J(et al)(agent)**  
Blakely, Sokoloff, Taylor & Zafman, 7th Floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025; US;

	Country	Number	Kind	Date
Patent	WO	200303313	A2-A3	20030109
Application	WO	2002US20781		20020628
Priorities	US	2001895529		20010629

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)

[EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;  
GR; IE; IT; LU; MC; NL; PT; SE; TR;

[OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;  
ML; MR; NE; SN; TD; TG;

[AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;  
UG; ZM; ZW;

[EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

**Language** Publication Language: English

Filing Language: English

Fulltext word count: 9527

**Detailed Description:**

...or off. In an embodiment, when the time-slice enable bit 428 of the unit **register** 414 is clear, fixed ring buffer priorities are in effect. In an embodiment, when the...the activation or resumption of an instruction-stream 413 supplying instructions to a specific

ring **buffer memory** area, such as the first **ring buffer memory** area 430, causes the timer countdown field (TC) 434 to be initialized with the content value in the time slice **register** 5 420, 422 portion of that specific **ring buffer**, such as the first **ring buffer register** 420. The timer countdown field 434 decrements every time-unit while the execution of the instructions from the **ring buffer memory** area continues.

[0050] The time slice counter 418 decrements the timer countdown field 434 every time unit. The unit time counter 416 monitors and counts every core clock cycle. The unit time counter 416 sends a signal to the time slice counter 418 to decrement the timer...

Dialog eLink: [Order File History](#)

17/3K/11 (Item 11 from file: 349)

DIALOG(R)File 349: PCT FULLTEXT

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00974197

## **APPARATUS, METHOD AND SYSTEM WITH A GRAPHICS-RENDERING ENGINE HAVING A GRAPHICS CONTEXT MANAGER**

APPAREIL, PROCEDURE ET SYSTEME FAISANT APPEL A UN MOTEUR DE RENDU GRAPHIQUE PRESENTANT UN GESTIONNAIRE DE CONTEXTES

### **Patent Applicant/Patent Assignee:**

- **INTEL CORPORATION**; (a Delaware Corporation), 2200 Mission College Boulevard, Santa Clara, CA 95052  
US; US(Residence); US(Nationality)

### **Legal Representative:**

- **MALLIE Michael J(et al)(agent)**  
Blakely, Sokoloff, Taylor & Zafman LLP, 12400 Wilshire Boulevard, 7th floor, 7th Floor, Los Angeles, CA 90025; US;

	Country	Number	Kind	Date
Patent	WO	200303206	A2-A3	20030109
Application	WO	2002US20682		20020628
Priorities	US	2001895777		20010629

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)



[EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;  
GR; IE; IT; LU; MC; NL; PT; SE; TR;

[OA] BF; BJ; CF; CG; CI; CM; GA; GN; GQ; GW;  
ML; MR; NE; SN; TD; TG;

[AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;  
UG; ZM; ZW;

[EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

**Language** Publication Language: English

Filing Language: English

Fulltext word count: 9131

### Detailed Description:

...or off. In an embodiment, when the time-slice enable bit 428 of the unit **register** 414 is clear, fixed ring buffer priorities are in effect. In an embodiment, when the...the activation or resumption of an instruction-stream 413 supplying instructions to a specific **ring buffer memory** area, such as the first **ring buffer memory** area 430, causes the timer countdown field (TC) 434 to be initialized with the content value in the time slice **register** 5 420, 422 portion of that specific **ring buffer**, such as the first **ring buffer register** 420. The timer countdown field 434 decrements every time-unit while the execution of the instructions from the **ring buffer memory** area continues.

[00501 The time slice counter 418 decrements the ...434 every time unit. The unit time counter 416 monitors and counts every core clock **cycle**. The unit **time** counter 416 sends a signal to the time slice counter 418 to decrement the timer...

26/3K/1 (Item 1 from file: 348)  
DIALOG(R)File 348: EUROPEAN PATENTS  
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01801703

**Intelligent video information management system**  
Intelligentes Videoinformationsverwaltungssystem  
Systeme intelligent pour gerer des informations video

**Patent Assignee:**

- **Sensormatic Electronics Corporation;** (882795)  
6600 Congress Avenue; Boca Raton, Florida 33487; (US)  
(Applicant designated States: all)

**Inventor:**

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- **MacCormack, David, Ross**  
3344 31st Street; San Diego, California 92104-4620; (US)
- **Wilson, Charles, Park**  
9807 Highdale Road; Santee, CA 92701; (US)
- **Winter, Gerhard Josef**  
7408 Park Village Road; San Diego, CA 92129-4541; (US)

**Legal Representative:**

- **Hafner, Dieter, Dr. et al (52276)**  
Hafner & Partner GbR Patent-/Rechtsanwalte Schleiermacherstrasse 25; 90491  
Nurnberg; (DE)

	Country	Number	Kind	Date	
Patent	EP	1471464	A2	20041027	(Basic)
Application	EP	2004004117		19971001	
Priorities	US	742017		19961031	
	US	741715		19961031	
	US	740628		19961031	
	US	741982		19961031	
	US	741914		19961031	
	US	741983		19961031	

	Country	Number	Kind	Date
	US	729620		19961031
	US	740651		19961031
	US	742015		19961031
	US	741650		19961031
	US	740627		19961031

**Designated States:**

DE; FR; GB; SE;

**Related Parent Numbers: Patent (Application):**EP 1010315 (EP 97910786)

**International Patent Class (V7):** G06T-007/20**Abstract Word Count:** 139

**NOTE:** NONE

**NOTE: Figure number on first page:** NONE

Legal Status	Type	Pub. Date	Kind	Text

**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	200444	2925
SPEC A		(English)	200444	75072
Total Word Count (Document A) 77997				
Total Word Count (Document B) 0				
Total Word Count (All Documents) 77997				

**Specification:** ...frames using a first predetermined video data analysis algorithm to generate first analysis data, a **storage** device connected to the A/D circuitry and the first analysis means, for storing the... ..analysis data generated by the first analysis circuitry, and second analysis circuitry, connected to the **storage** device, for retrieving the sequence of video data frames stored in the **storage** device and for analyzing the retrieved sequence of video data frames using a second predetermined... ..analysis algorithm to generate second analysis data, the second analysis data being stored in the **storage** device. The apparatus may also include a display device, connected to the **storage** device, for displaying at least one image represented by the stored **sequence** of images. The **storage** device may include compression circuitry for receiving the sequence of video data frames from the... ..algorithm to the sequence of video data frames to form compression video data, and a **storage** medium on which the compression video data is stored. The **storage** medium may be a hard disk, magneto-optical disk, writable optical disk or other conventional mass **storage** device. The first analysis circuitry may include a first processing device and the second analysis... ..using a single processor which

performs a first analysis on the video data prior to **storage** and performs a second analysis on the video data after the video data has been stored and then retrieved from the **storage** device.

According to yet a further aspect of the invention, there is provided a video...display device displays the images corresponding to the stream of video data stored in the **storage** device, and selecting for updating a portion of an image plane corresponding to the image... ..analysis algorithm to the stream of dynamic images, in response to detection of an alarm **condition**. The alarm **condition** may be detected by application of the analysis algorithm, and the algorithm may be for... ..each generated by a respective video camera, including the steps of determining whether an alarm **condition** is present, and in the case that no alarm **condition** is present, recording images generated by the video cameras on the recording medium at a... ..recorded on the recording medium per unit time, and in the case that an alarm **condition** is determined to be present, recording images generated by the video cameras on the recording... ..is higher than the first aggregate image rate. The step of determining whether an alarm **condition** is present may include analyzing content of at least one of the streams of video... ..the invention, the aggregate image recording rate is 30 images per second when no alarm **condition** is present and is 45 images per second when an alarm **condition** is present.

According to an

Dialog eLink: [Order File History](#)

26/3K/12 (Item 12 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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00923209

### **Video/audio coding and multiplexing method and apparatus**

Video/Audiokodierungs- und Multiplexierungsverfahren und -vorrichtung

Methode et appareil pour le codage et multiplexage de signaux video et audio

### **Patent Assignee:**

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(Applicant designated States: all)

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**Legal Representative:**

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	Country	Number	Kind	Date
Patent	EP	841819	A2	19980513 (Basic)
	EP	841819	A3	20041124
Application	EP	97119695		19971111
Priorities	JP	96298970		19961111
	JP	96302582		19961114
	JP	97162255		19970619

**Designated States:**

DE; FR; GB;

**Extended Designated States:**

AL; LT; LV; MK; RO; SI;

**International Patent Class (V7):** H04N-007/52; H04N-009/877  
**Abstract Word Count:** 151

**NOTE:** 1

**NOTE:** Figure number on first page: 1

Legal Status	Type	Pub. Date	Kind	Text
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**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	9820	3370
SPEC A		(English)	9820	27087
Total Word Count (Document A) 30457				
Total Word Count (Document B) 0				
Total Word Count (All Documents) 30457				

**Specification:** ...buffer, and the read-out coded multimedia information is deleted. In this method, the temporary **storage** of coded information is carried out using the **ring buffer** method.

According to a third aspect of the invention, in the method according to the... ..the reproduction time T included in the block rate information is used as the synchronous **interval time** information, and this is output to the coded multimedia information read-out step.

According to ...buffer, and the read-out coded multimedia information is deleted. In this method, the temporary **storage** of coded information is carried out using the **ring buffer** method.

According to an eighth aspect of the invention, the method according to the fifth... ..a storage position error detection process and a storage position error correction process, at prescribed **time intervals**. The storage position error detection process is to detect a difference between an audio storage...reuse of the ring buffer so as to replace the positions of video and audio **memory** blocks with correct ones. However, when such calculations are performed, the effect of the apparatus... ..the same in size, being restricted by the reproduction processing system. Further, some computer systems **request** that video and audio memory blocks are aligned with **boundaries** efficient for processing (e.g. word **boundaries**, or units of multiple of sectors in hard disk). It is difficult to realize the...can prevent the above-mentioned problem relating to the positional error in video and audio **memory** blocks, by performing a correction process.

Figure 22 is a block diagram showing the structure... ..according to this eighth embodiment. As shown in figure 22, in this eighth embodiment, a **memory** block error correction means 901 is added to the same structure as the apparatus according to the sixth embodiment. The **memory** block error correction means 901 operates intermittently at **time intervals**, and performs an error detection process and an error correction process. In the error detection process, the **memory** block error correction means 901 detects an audio **memory** block in which coded audio information to be reproduced at a reproduction time is stored and a video **memory** block in which coded video information to be reproduced at the reproduction time is stored, and obtains a difference in locations between the audio **memory** block and the video **memory** block in the video/audio buffer 902, as a **memory** block error. In the error correction process, it inserts dummy data by a quantity equivalent to the detected error, in the video **memory** block or the audio **memory** block. The dummy data used in the correction process has no meaning for a decoder... ..is not 1.36:1 but 1.4:1. So, due to repetition of the **ring buffer** operation, a positional error occurs between a video **memory** block and an audio memory block which are to be reproduced at the same time... ..order to solve this problem, the memory block error correction means 901 functions at set **time intervals**, and inserts dummy data in the video memory block so that the video memory block...

**Claims:** ...interval time information which is obtained by using the block rate information and shows a **time interval** in the **time** division multiplexing; and

coded multimedia information synchronization step in which multimedia data coded and multiplexed... ..in the coded multimedia information buffering step, the temporary

storage is carried out with the **memory** unit for temporary **storage** being a **ring buffer**;  
and

in the coded multimedia information read-out step, the read-out is carried out with the **memory** unit for temporary **storage** being a **ring buffer**, and the read-out coded multimedia information is deleted.

3. The method of claim 1... ..the reproduction time T included in the block rate information is used as the synchronous **interval time** information, and this is output to the coded multimedia information read-out step.

4. The... ..in the coded multimedia information synchronization step, the read-out is carried out with the **memory** unit for temporary **storage** being a **ring buffer**, and the read-out coded multimedia information is deleted.

8. The method of claim 5... ..a storage position error detection process and a storage position error correction process, at prescribed **time intervals**;

wherein said storage position error detection process is to detect a difference between an audio...

Dialog eLink: [Order File History](#)

26/3K/13 (Item 13 from file: 348)

DIALOG(R)File 348: EUROPEAN PATENTS

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00694339

**METHOD AND DEVICE FOR REPRODUCING DATA**  
**VERFAHREN UND VORRICHTUNG ZUR DATENWIEDERGABE**  
**PROCEDE ET APPAREIL DE REPRODUCTION DE DONNEES**

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	Country	Number	Kind	Date	
Patent	EP	671738	A1	19950913	(Basic)
	EP	671738	A1	19960807	
	EP	671738	B1	20010711	
	WO	9509421		19950406	
Application	EP	94927795		19940928	
	WO	94JP1604		19940928	
Priorities	JP	93242532		19930929	

**Designated States:**

DE; FR; GB;

**International Patent Class (V7):** G11B-020/18; H03M-013/00; H04N-009/804  
**Abstract Word Count:** 172

Legal Status	Type	Pub. Date	Kind	Text
--------------	------	-----------	------	------

**Language** Publication: English

Procedural: English

Application: Japanese

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS A		(English)	EPAB95	1363
SPEC A		(English)	EPAB95	10856
CLAIMS B		(English)	200128	831
CLAIMS B		(German)	200128	707
CLAIMS B		(French)	200128	1007
SPEC B		(English)	200128	9963
Total Word Count (Document A) 12220				
Total Word Count (Document B) 12508				
Total Word Count (All Documents) 24728				

**Specification:** ...that optical disc 1 makes one rotation for a second time, i.e., for a **time period** until sector No. obtained from sector detecting circuit 4 becomes equal to sector



No, at the time of track jump, writing into **ring buffer memory 5** of new data is inhibited, and data already stored in the **ring buffer memory 5** is transferred, as occasion demands, to video code buffer memory 10.

Moreover, even if...both the outer code and the inner code.

Write and read operations with respect to **ring buffer memory 5** are carried out at a rate several times, e.g., five times... ..required when data of one sync temporarily stored in buffer memory 61 is written into **ring buffer memory 5**. Xc indicates rewrite time period of error symbol and outer code flag. Yr indicates read-out time period of inner code data on the **ring buffer memory 5**. Yc indicates rewrite time period of error symbol and inner code flag. Zr indicates read-out **time period** to video code buffer **memory 10** of data on the **ring buffer memory 5**.

FIG. 11 shows, in a model form, **memory space** of **ring buffer memory 5** with one code length being set to one row (line) in lateral direction. In... ..buffer will be described below with reference to the timing chart of FIG. 10.

At **time period Xw** of FIG. 10, when data of one row that pointer PXw of FIG. 11 indicates, which is stored in buffer **memory 61**, is written into **ring buffer memory 5**, interlocking selector contacts 63, 64 of switch 62 are both connected to contact a... ..As the result of correction, error symbol and its correction value are obtained. Then, at **time period Xc** of FIG. 10, error correcting circuit 71 delivers corrected value to **ring buffer memory 5** and flag register 73. At **ring buffer memory 5**, overwrite operation of corrected value is carried out with respect to corresponding error symbol... ..number of errors is great and correction is impossible, rewrite operation of data on the **ring buffer memory 5** is not carried out to judge this state to be correction inability state, thus to write 1 into outer code flag  $fi+144$ .

Then, at the **time period Yr** of FIG. 10, selector contacts 63, 64 of switch 62 are switched to contact b, and data is read out, for inner code correction, from the **ring buffer memory 5** in the interleaving direction from the position indicated by pointer PYr, i.e., the... length (144).

The reason why although outer code flag is written into vacant area of **ring buffer memory 5**, it is stored into flag register 73 and is delivered to error correcting circuit 71 is that it is impossible to... ..the same time, data and outer code flag every symbols from different areas within the **ring buffer memory 5**.

Then, at **time period Yc** of FIG. 10, similarly to the case of correction of outer code, corrected value is sent into **ring buffer memory 5** with respect to error symbol on the **ring buffer memory 5** and is overwritten thereonto. If the number of errors is great to such an... ..codes exist when viewed from outer code direction is inner code flag.

Data on the **ring buffer memory 5** in which error correction has been completed in this way is eventually read out to video code buffer memory 10 of decode section 20 at the

**time period** Zr of FIG. 10. PZr of FIG. 11 is read pointer in this case. Data...for a second time. Write operation of data from sector of A32 into ring buffer **memory 5** is restarted.

At this time, there may be instances where A32 is invalid sector... ..carrying out the above-mentioned operation, it is possible to successively write continuous data into **ring buffer memory 5** although write operation is interrupted by track jump.

Since pointers PXw, PYr (PYe) are stopped by track jump, but data are read out in accordance with **request** of video code buffer **memory 10** for this **time period**, pointer PZr is advanced in counterclockwise direction in FIG. 15 and data remaining quantity of **ring buffer memory 5** is decreased. Thus, overflow can be prevented.

Abnormal **condition** processing operation will now be described. It is now assumed that track jump that pick... ..cannot be obtained, flywheel is turned OFF. Thus, write and read operations of ring buffer **memory 5**, and error correcting operation of inner code of error correcting circuit 71 are interrupted ...

**Specification:** ...that optical disc 1 makes one rotation for a second time, i.e., for a **time period** until sector No. obtained from sector detecting circuit 4 becomes equal to sector No. at the time of track jump, writing into **ring buffer memory 5** of new data is inhibited, and data already stored in the **ring buffer memory 5** is transferred, as occasion demands, to video code buffer memory 10.

Moreover, even if...both the outer code and the inner code.

Write and read operations with respect to **ring buffer memory 5** are carried out at a rate several times, e.g., five times... ..required when data of one sync temporarily stored in buffer memory 61 is written into **ring buffer memory 5**. Xc indicates rewrite time period of error symbol and outer code flag. Yr indicates read-out time period of inner code data on the **ring buffer memory 5**. Yc indicates rewrite time period of error symbol and inner code flag. Zr indicates read-out **time period** to video code buffer **memory 10** of data on the **ring buffer memory 5**.

FIG. 11 shows, in a model form, **memory space** of **ring buffer memory 5** with one code length being set to one row (line) in lateral direction. In... ..buffer will be described below with reference to the timing chart of FIG. 10.

At **time period** Xw of FIG. 10, when data of one row that pointer PXw of FIG. 11 indicates, which is stored in buffer **memory 61**, is written into **ring buffer memory 5**, interlocking selector contacts 63, 64 of switch 62 are both connected to contact a.... ..As the result of correction, error symbol and its correction value are obtained. Then, at **time period** Xc of FIG. 10, error correcting circuit 71 delivers corrected value to **ring buffer memory 5** and flag register 73. At **ring buffer memory 5**, overwrite operation of corrected value is carried out with respect to corresponding error symbol... ..number of errors is great and correction is impossible, rewrite operation of data on the **ring buffer**

**memory 5** is not carried out to judge this state to be correction inability state, thus to write 1 into outer code flag fi+144.

Then, at the **time period** Yr of FIG. 10, selector contacts 63, 64 of switch 62 are switched to contact b, and data is read out, for inner code correction, from the **ring buffer memory 5** in the interleaving direction from the position indicated by pointer PYr, i.e., the ... length (144).

The reason why although outer code flag is written into vacant area of **ring buffer memory 5**, it is stored into flag **register 73** and is delivered to error correcting circuit 71 is that it is impossible to... ..the same time, data and outer code flag every symbols from different areas within the **ring buffer memory 5**.

Then, at **time period** Yc of FIG. 10, similarly to the case of correction of outer code, corrected value is sent into **ring buffer memory 5** with respect to error symbol on the **ring buffer memory 5** and is overwritten thereonto. If the number of errors is great to such an... ..codes exist when viewed from outer code direction is inner code flag.

Data on the **ring buffer memory 5** in which error correction has been completed in this way is eventually read out to video code buffer memory 10 of decode section 20 at the **time period** Zr of FIG. 10. PZr of FIG. 11 is read pointer in this case. Data...for a second time. Write operation of data from sector of A32 into ring buffer **memory 5** is restarted.

At this time, there may be instances where A32 is invalid sector... ..carrying out the above-mentioned operation, it is possible to successively write continuous data into **ring buffer memory 5** although write operation is interrupted by track jump.

Since pointers PXw, PYr (PYe) are stopped by track jump, but data are read out in accordance with **request** of video code buffer **memory 10** for this **time period**, pointer PZr is advanced in counterclockwise direction in FIG. 15 and data remaining quantity of **ring buffer memory 5** is decreased. Thus, overflow can be prevented.

Abnormal **condition** processing operation will now be described. It is now assumed that track jump that pick be obtained, flywheel is turned OFF. Thus, write and read operations of ring buffer **memory 5**, and error correcting operation of inner code of error correcting circuit 71 are interrupted ...

**Digital data processing system.**

Digitales Datenverarbeitungssystem.

Système de traitement de données numériques.

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**Legal Representative:**

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	Country	Number	Kind	Date	
Patent	EP	290111	A2	19881109	(Basic)
	EP	290111	A3	19890503	
	EP	290111	B1	19931222	
Application	EP	88200917		19820521	
Priorities	US	266404		19810522	

**Designated States:**

AT; BE; CH; DE; FR; GB; IT; LI; LU; NL;  
SE;

**Related Parent Numbers: Patent (Application):**EP 67556 (EP 823025960)

**International Patent Class (V7):** G06F-009/30; ; **Abstract Word Count:** 123

Legal Status Type	Pub. Date	Kind	Text
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**Language** Publication: English

Procedural: English

Application: English

Fulltext Availability	Available Text	Language	Update	Word Count
CLAIMS B		(English)	EPBBF1	1044
CLAIMS B		(German)	EPBBF1	890
CLAIMS B		(French)	EPBBF1	1185
SPEC B		(English)	EPBBF1	154314
Total Word Count (Document A) 0				
Total Word Count (Document B) 157433				
Total Word Count (All Documents) 157433				

**Specification:** ...may contain local data, that is data generated during execution of a program. Each Procedure **Object**, the **Static** Data Area and the Macro-stack are individual Objects identified by UIDs and AONs and ...descriptor comprised of a 14 bit AON field, a 32 bit Offset field, and **Length field**, wherein 32 bit logical descriptor Offset field is divided into a 18 bit Page Number (P) field and a 14 bit Offset within **Page** (O) field. In translating a logic1 into a physical descriptor, logical descriptor Length and O **descriptors** for which ATU 10228 has corresponding entries. Because physical descriptor FN fields are provided from... ..cache, rather than directly as in physical descriptor O and Length fields, a physical descriptor's FN field will be provided to MEM 10112, for example, one clock cycle later than...

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26/3K/15 (Item 15 from file: 349)

DIALOG(R)File 349: PCT FULLTEXT

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00969934

**METHOD AND APPARATUS FOR DATA COMPRESSION OF MULTI-CHANNEL MOVING PICTURES**

PROCEDE ET APPAREIL DE COMPRESSION DE DONNEES D'IMAGES ANIMEES A PLUSIEURS CANAUX

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(Designated only for: US)
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**Legal Representative:**

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	Country	Number	Kind	Date
Patent	WO	2002104035	A1	<B>20021227</B>
Application	WO	2002KR1118		20020614
Priorities	KR	200133887		20010615

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)

[EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;  
GR; IE; IT; LU; MC; NL; PT; SE; TR;

**Language** Publication Language: English

Filing Language: Korean

Fulltext word count: 2805

**Detailed Description:**

...with the present invention provides the compression processor 140 with the information about the frame **parameters** corresponding to each frame. The compression processor 140 is then able to distinguish each channel...video images while the memories 150 and 160 for storing the reference image and frame **parameters** are prepared as many as the number of channels in a separate manner.

FIG.2... ..structure of the buffer memory for the reference image. Referring to FIG.2, the buffer **memory** for the reference image 150, which is also called as a first **memory** in Claim 1, has **memory** banks 200 as many as the number of channels.

In addition, both the reconstruction  
buffer...frame.

FIG.3 is a schematic diagram  
illustrating the constitution of the capture  
1 5

**memory** for video image as an embodiment in  
accordance with the present invention.

Referring to FIG.3, it should be noted that the  
capture **memory** is not assigned to each channel.

Instead, only a certain number of  
capture **memories** (for instance, three) are  
prepared and they are used for the common  
capture buffer. The common capture buffer can  
be employed as a type of **ring buffer in time**  
**sequence** in an effort to minimize the **memory**  
2 5 1  
requirement.

Referring to FIG.1 again, a moving  
picture compression processor 140... ..capture buffer.

Further, the compression processor 140  
processes the MPEG stream by referring to the  
**memory** bank 150, 160 for each channel at the  
instant of MPEG encoding process.

Furthermore, the transfer of the MPEG  
encoded data to the main processor is performed  
after the **request** of the moving picture  
compression processor 140 wherein the channel  
information or the accompanying frame **parameters**  
should be recognized beforehand for the MPEG  
encoded data at stand by.

Thereafter, the MPEG...



00781825

**SYSTEM OF REUSABLE SOFTWARE PARTS AND METHODS OF USE**  
**SYSTEME D'UNITES LOGICIELLES REUTILISABLES ET PROCEDES**  
**D'UTILISATION**

**Patent Applicant/Patent Assignee:**

- **Z-FORCE CORPORATION**; 151 Kalmus Drive, Suite B-250, Costa Mesa, CA 92626  
US; US(Residence); US(Nationality)

**Legal Representative:**

- **TACHNER Adam H(et al)(agent)**  
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	Country	Number	Kind	Date
Patent	WO	200114959	A2-A3	<B>20010301</B>
Application	WO	2000US22694		20000816
Priorities	US	99149371		19990816
	US	99149624		19990816

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)

[EP] AT; BE; CH; CY; DE; DK; ES; FI; FR; GB;  
GR; IE; IT; LU; MC; NL; PT; SE;

[OA] BF; BJ; CF; CG; CI; CM; GA; GN; GW; ML;  
MR; NE; SN; TD; TG;

[AP] GH; GM; KE; LS; MW; MZ; SD; SL; SZ; TZ;  
UG; ZW;

[EA] AM; AZ; BY; KG; KZ; MD; RU; TJ; TM;

**Language** Publication Language: English

Filing Language: English

Fulltext word count: 182432

**Detailed Description:**

...a signaled state.

6. Part A arms DM

EVS passing the event object, a timeout **period**  
and a context associated with the event object.

7. Immediately, DM

EVS's worker thread...

Dialog eLink: [Order File History](#)

26/3K/17 (Item 17 from file: 349)

DIALOG(R)File 349: PCT FULLTEXT

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00546702

**MULTI-PERSPECTIVE VIEWER FOR CONTENT-BASED INTERACTIVITY**  
VISUALISEUR MULTIDIMENSIONNEL OFFRANT UNE INTERACTIVITE  
ORIENTEE OBJET

**Patent Applicant/Patent Assignee:**

- **PRAJA INC;**
- ;;

	Country	Number	Kind	Date
Patent	WO	200010075	A1	<B>20000224</B>
Application	WO	99US18376		19990812
Priorities	US	98134188		19980814

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)

**Language** Publication Language: English

Filing Language:

Fulltext word count: 21458

**Detailed Description:**

...presence system 200 can deduce the identity of the unknown object by using static model **constraints** and heuristic information. For example, it might deduce that region 2

is a new object... ..extent, and movement information of all objects together. If the state is maintained for a **period of time**, the EM effectively has an in-**memory** spatial-temporal database. This database can be used to process user **queries** involving **static** and dynamic objects, space, and 2 0 time. Some example queries that may be processed...

Dialog eLink: [Order File History](#)

26/3K/18 (Item 18 from file: 349)

DIALOG(R)File 349: PCT FULLTEXT

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00428986

## INTELLIGENT VIDEO INFORMATION MANAGEMENT SYSTEM SYSTEME INTELLIGENT POUR GERER DES INFORMATIONS VIDEO

**Patent Applicant/Patent Assignee:**

- **SENSORMATIC ELECTRONICS CORPORATION;**  
;;

	Country	Number	Kind	Date
Patent	WO	9819450	A2	<B>19980507</B>
Application	WO	97US17886		19971001
Priorities	US	96742017		19961031
	US	96741715		19961031
	US	96740628		19961031
	US	96741982		19961031
	US	96741914		19961031
	US	96741983		19961031
	US	96729620		19961031
	US	96740651		19961031
	US	96742015		19961031
	US	96741650		19961031
	US	96740627		19961031

**Designated States:** (All protection types applied unless otherwise stated - for applications 2004+)

**Language** Publication Language: English

Filing Language:

Fulltext word count: 114725

**Detailed Description:**

...stream of images by the image selection circuitry, playback circuitry for selectively retrieving from the **storage** device the stored images from the first and second streams of images, and 35 analysis...difference-encoded image data frames, storing all of the reconstituted image data frames in a **storage** device, and retrieving the reconstituted image data frames from the **storage** device and displaying the retrieved image data frames, with a plurality of the reconstituted difference-encoded image data frames being stored in the **storage** device before any of the reconstituted difference-encoded image data frames are retrieved from the **storage** device and displayed.

**SUBSTITUTE SHEET (RULE 26)**

According to still another aspect of the invention...

15/9/8 (Item 6 from file: 350)  
DIALOG(R)File 350: Derwent WPIX  
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0014232427 *Drawing available*  
WPI Acc no: 2004-418367/200439  
XRPX Acc No: N2004-332004

**Paper jam detector for image forming device e.g. laser printer associates signal indicating status of image forming unit with time series data in storage unit**

Patent Assignee: BROTHER KOGYO KK (BRER)

Inventor: ENDO Y

Patent Family ( 4 patents, 33 countries )							
Patent Number	Kind	Date	Application Number	Kind	Date	Update	Type
US 20040086284	A1	20040506	US 2003694750	A	20031029	200439	B
EP 1418533	A2	20040512	EP 200324870	A	20031031	200439	E
JP 2004148690	A	20040527	JP 2002316953	A	20021031	200441	E
US 7140707	B2	20061128	US 2003694750	A	20031029	200680	E

Priority Applications (no., kind, date): JP 2002316953 A 20021031; US 2003694750 A 20031029

**Publication No.** EP 1418533 A2 (Update 200439 E)

Original Abstract:

By executing a storage control program, a CPU core (59) stores in a time series detection signals from sensors (58), operation signals outputted to operating members (57), and error detection results in a ring buffer (73) of a RAM (55) whenever a change occurs in any of these data. The CPU core (59) outputs the data stored in the ring buffer to a personal computer (74), for example, based on an output program. Accordingly, an operator can view the history of detection signals outputted from the sensors (58) and the operation signals outputted to the operating members (57) up to the point that the error is generated on the CRT screen of the personal computer (74). The operator can easily analyze the cause of the generated error.

File 347: JAPIO Dec 1976-2009/Jan(Updated 090503)

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File 350: Derwent WPIX 1963-2009/UD=200937

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Set Items Description

S1 2831788 REGISTER? ? OR STORAGE OR MEMOR??? OR RAM OR DRAM OR SRAM OR SDRAM OR  
RDRAM OR SLDRAM OR SGRAM OR DRDRAM OR ROM OR PROM OR EPROM OR EEPROM

S2 1271 RING()BUFFER? ?

S3 490690 (TIME OR TIMING)(3N)(SERIES OR SEQUENCE? ? OR PERIOD? ? OR LENGTH OR  
DURATION OR SPAN???? OR INTERVAL? ? OR CYCLE? ?) OR TIMESPAN? ?

S4 175521 S1(7N)(WINDOW? ? OR DISTANCE? ? OR GAP OR GAPS OR SUBSET? ? OR PORTION? ?  
OR SEGMENT? ? OR SECTION? ? OR PIECE? ?)

S5 1120595 STATISTIC?? OR DIAGNOSTIC?? OR MEASUREMENT? ? OR MEASURES OR  
PERFORMANCE()DATA OR METRIC? ?

S6 356533 QUERY OR QUERIES OR REQUEST? ?

S7 408 S6(3N)(PERSISTENT OR STATIC)

S8 2296082 CONDITION? ? OR CRITERIA OR CRITERION OR PARAMETER? ? OR PREFERENCE? ? OR  
CONSTRAINT? ? OR BOUNDAR??? OR ARGUMENT? ?

S9 563 S1(10N)S2

S10 38 S9 AND S3

S11 9 S10 AND S4

S12 70608 S5(10N)(WINDOW? ? OR DISTANCE? ? OR GAP OR GAPS OR SUBSET? ? OR PORTION?  
? OR SEGMENT? ? OR SECTION? ? OR PIECE? ?)

S13 0 S11 AND S12

S14 4 REGISTER? ?(10N)S2 AND S3

S15 13 S11 OR S14

S16 3 S7 AND S1 AND S3 AND S8

S17 173 S6(20N)S1(20N)S3(20N)S8

S18 0 S9 AND S17

File 348: EUROPEAN PATENTS 1978-200925

(c) 2009 European Patent Office

File 349: PCT FULLTEXT 1979-2009/UB= 20090611|UT= 20090604

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Set	Items	Description
S1	942870	REGISTER? ? OR STORAGE OR MEMOR??? OR RAM OR DRAM OR SRAM OR SDRAM OR RDRAM OR SLDRAM OR SGRAM OR DRDRAM OR ROM OR PROM OR EPROM OR EEPROM
S2	1255	RING()BUFFER? ?
S3	626245	(TIME OR TIMING)(3N)(SERIES OR SEQUENCE? ? OR PERIOD? ? OR LENGTH OR DURATION OR SPAN???? OR INTERVAL? ? OR CYCLE? ?) OR TIMESPAN? ?
S4	126662	S1(7N)(WINDOW? ? OR DISTANCE? ? OR GAP OR GAPS OR SUBSET? ? OR PORTION? ? OR SEGMENT? ? OR SECTION? ? OR PIECE? ?)
S5	739858	STATISTIC?? OR DIAGNOSTIC?? OR MEASUREMENT? ? OR MEASURES OR PERFORMANCE()DATA OR METRIC? ?
S6	2254285	QUERY OR QUERIES OR REQUEST? ?
S7	997	S6(3N)(PERSISTENT OR STATIC)
S8	1723721	CONDITION? ? OR CRITERIA OR CRITERION OR PARAMETER? ? OR PREFERENCE? ? OR CONSTRAINT? ? OR BOUNDAR??? OR ARGUMENT? ?
S9	633	S1(10N)S2
S10	79	S9(50N)S3
S11	11	S9(50N)S3(50N)S4
S12	75	REGISTER? ?(10N)S2
S13	11	S11(50N)S3
S14	84530	S5(10N)(WINDOW? ? OR DISTANCE? ? OR GAP OR GAPS OR SUBSET? ? OR PORTION? ? OR SEGMENT? ? OR SECTION? ? OR PIECE? ?)
S15	2	(S11 OR S13) AND S14
S16	11	S11 OR S13 OR S15
S17	11	IDPAT (sorted in duplicate/non-duplicate order)
S18	6	S7(50N)S3(50N)S1(50N)S8
S19	3996	S6(50N)S3(50N)S1(50N)S8
S20	26	S9 AND S19
S21	17	S10 AND S19
S22	23	S18 OR S21
S23	15	S22 AND (AC= US OR AC= US/PR) AND AY= 1978:2003
S24	7	S22 AND PY= 1978:2003
S25	18	S23:S24
S26	18	IDPAT (sorted in duplicate/non-duplicate order)
S27	46	PERSISTENT()QUER???
S28	0	S12 AND S27
S29	0	S9 AND S27

File 8: Ei Compendex(R) 1884-2009/Jun W1  
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 File 60: ANTE: Abstracts in New Tech & Engineer 1966-2009/Jun  
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Set Items Description

S1 2625953 REGISTER? ? OR STORAGE OR MEMOR??? OR RAM OR DRAM OR SRAM OR SDRAM OR RDRAM OR SLDRAM OR SGRAM OR DRDRAM OR ROM OR PROM OR EPROM OR EEPROM

S2 178 RING() BUFFER? ?

S3 729388 (TIME OR TIMING)(3N)(SERIES OR SEQUENCE? ? OR PERIOD? ? OR LENGTH OR DURATION OR SPAN???? OR INTERVAL? ? OR CYCLE? ? OR SLICE? ?) OR TIMESPAN? ?

S4 49605 S1(7N)(WINDOW? ? OR DISTANCE? ? OR GAP OR GAPS OR SUBSET? ? OR PORTION? ? OR SEGMENT? ? OR SECTION? ? OR PIECE? ?)

S5 9428738 STATISTIC?? OR DIAGNOSTIC?? OR MEASUREMENT? ? OR MEASURES OR PERFORMANCE() DATA OR METRIC? ?

S6 309876 QUERY OR QUERIES OR REQUEST? ?

S7 933 S6(3N)(PERSISTENT OR STATIC)

S8 12691032 CONDITION? ? OR CRITERIA OR CRITERION OR PARAMETER? ? OR PREFERENCE? ? OR CONSTRAINT? ? OR BOUNDAR??? OR ARGUMENT? ?

S9 89 S1 AND S2

S10 3 S9 AND S3

S11 0 S7 AND S1 AND S3 AND S8

S12 11391 S1 AND S3 AND S8

S13 117 S12 AND (QUERY OR QUERIES)

S14 0 S13 AND S9